

ARM Tools

**Configuration flags and settings for making program which
will run on the SmartBadge under Angel**

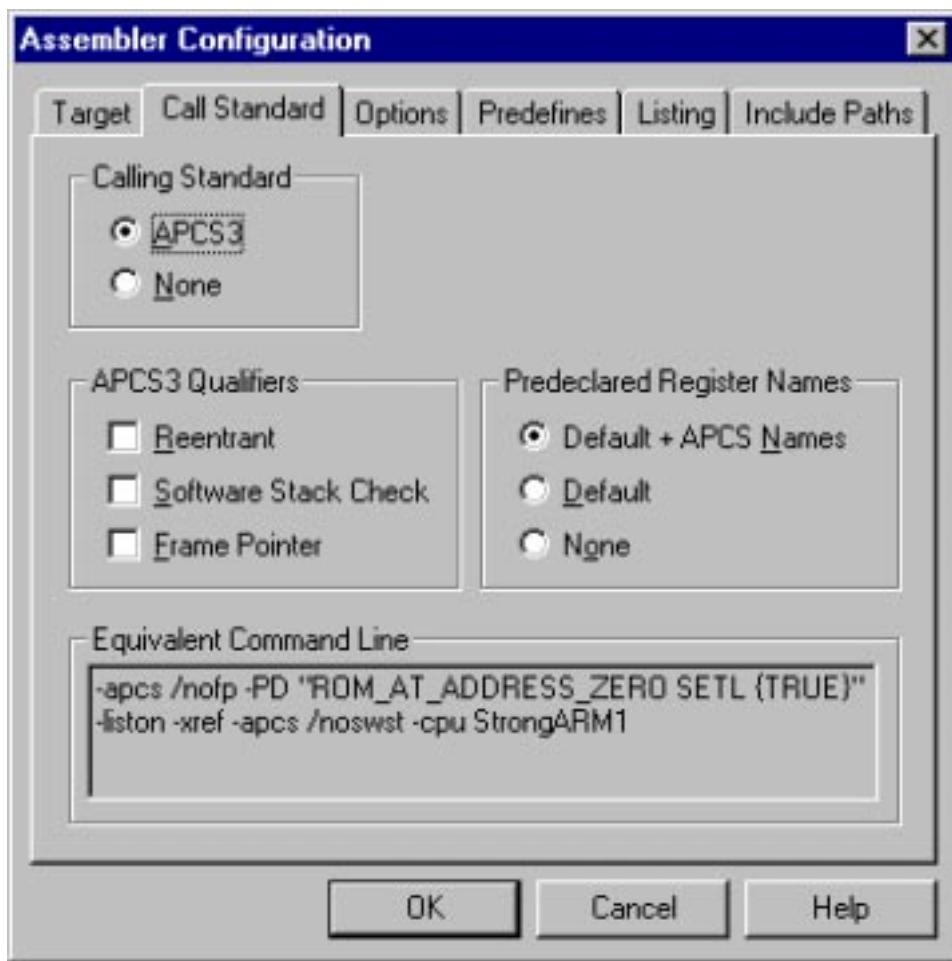
G. Q. Maguire Jr. (KTH) and M. T. Smith (HP)

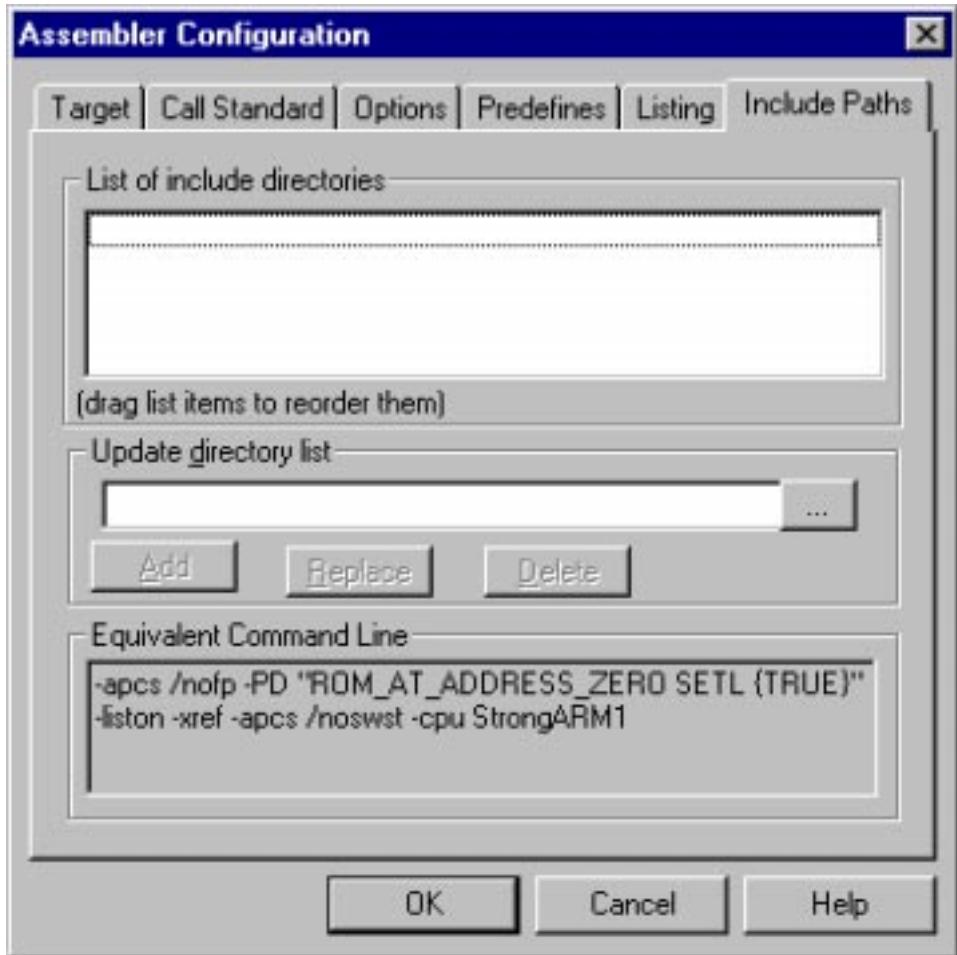
Version 0.1

1999.03.26

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Project Specific - settings





Assembler Configuration

Target | Call Standard | Options | Predefines | Listing | Include Paths |

Miscellaneous

- Listing on
- Verse
- XRef

Dimensions

Page Width

79

Page Length

66

Continuous Page

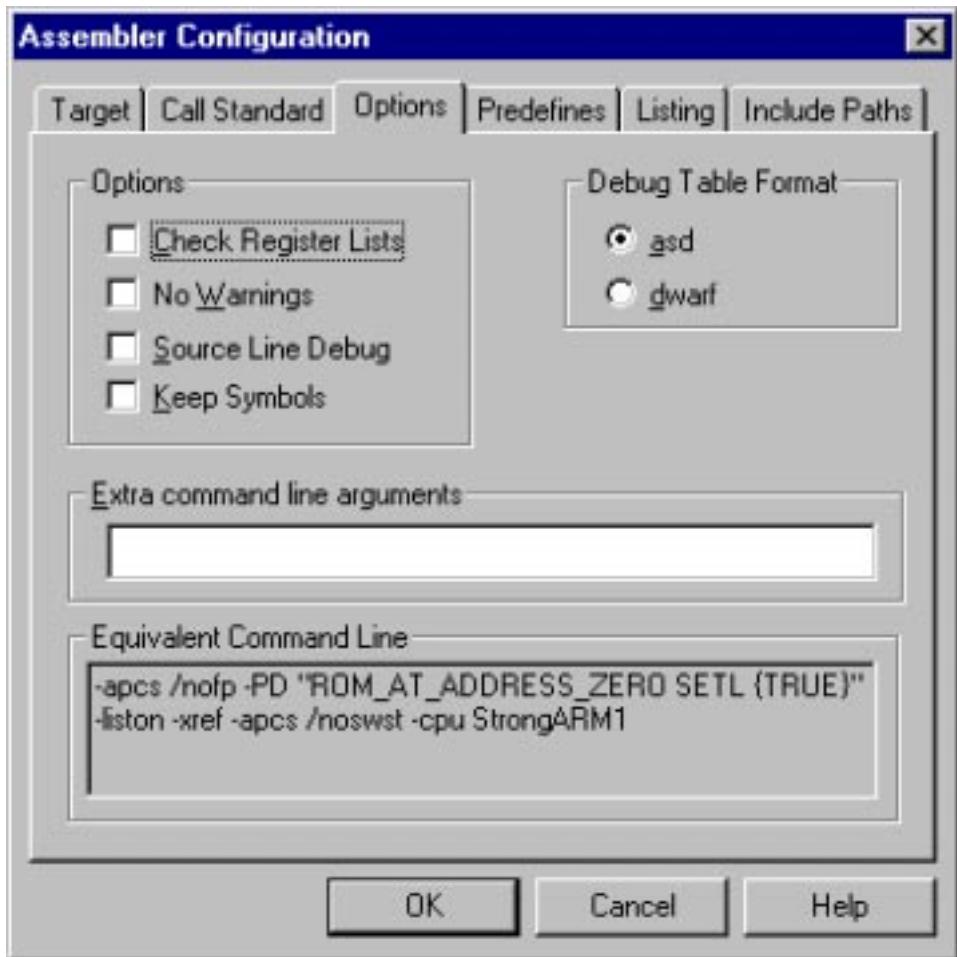
Equivalent Command Line

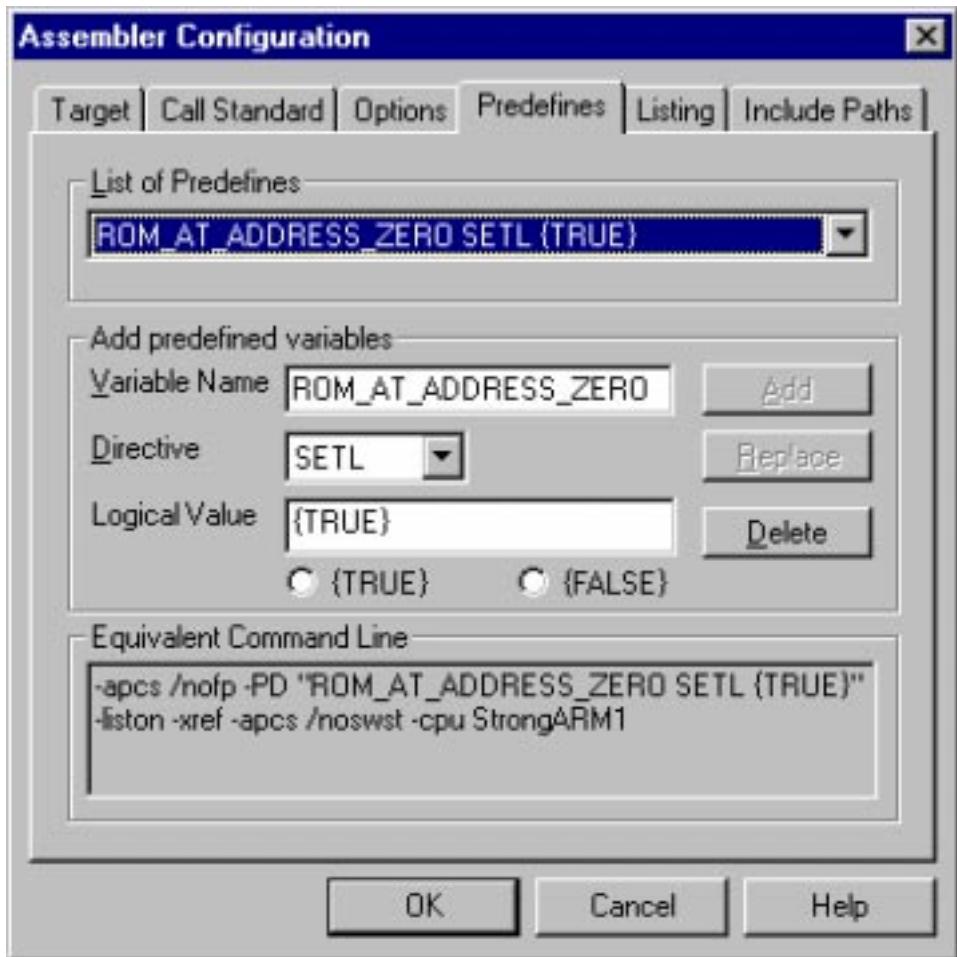
```
-apcs /nofp -PD 'ROM_AT_ADDRESS_ZERO SETL (TRUE)'  
-liston -xref -apcs /noswst -cpu StrongARM1
```

OK

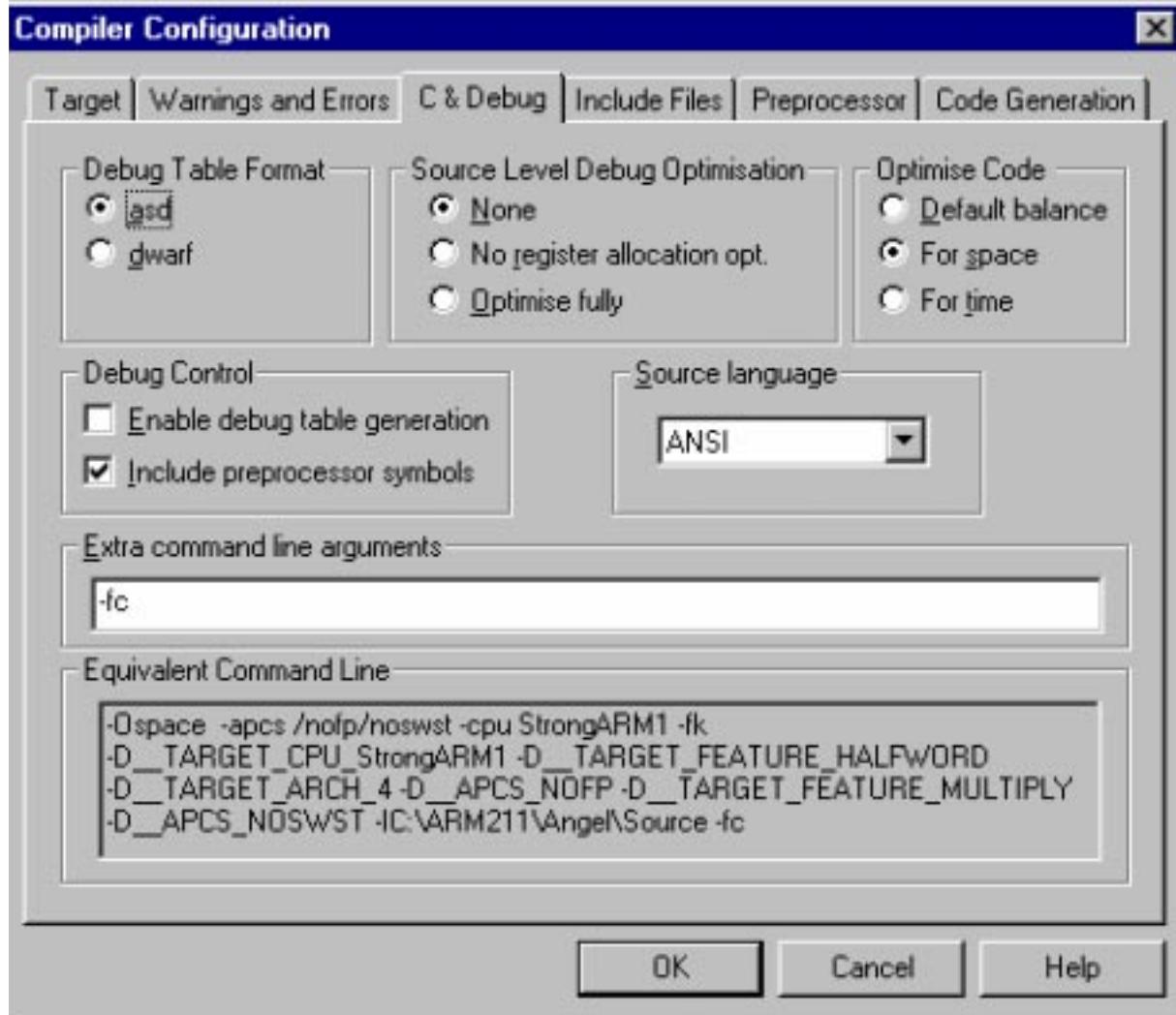
Cancel

Help









Compiler Configuration

Target | Warnings and Errors | C & Debug | Include Files | Preprocessor | Code Generation |

Generation Options

- | | |
|---|--|
| <input checked="" type="checkbox"/> Read-only literal strings | <input type="checkbox"/> Enum container always int |
| <input type="checkbox"/> Plain char always signed | <input type="checkbox"/> Functions always save link register |
| <input type="checkbox"/> One ADF area per function | <input type="checkbox"/> Access words only from word aligned addresses |

Latency

Max integer load

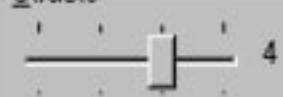
Max LDM Regs

Alignment

Top-Level Objects



Structs



Struct pointer align

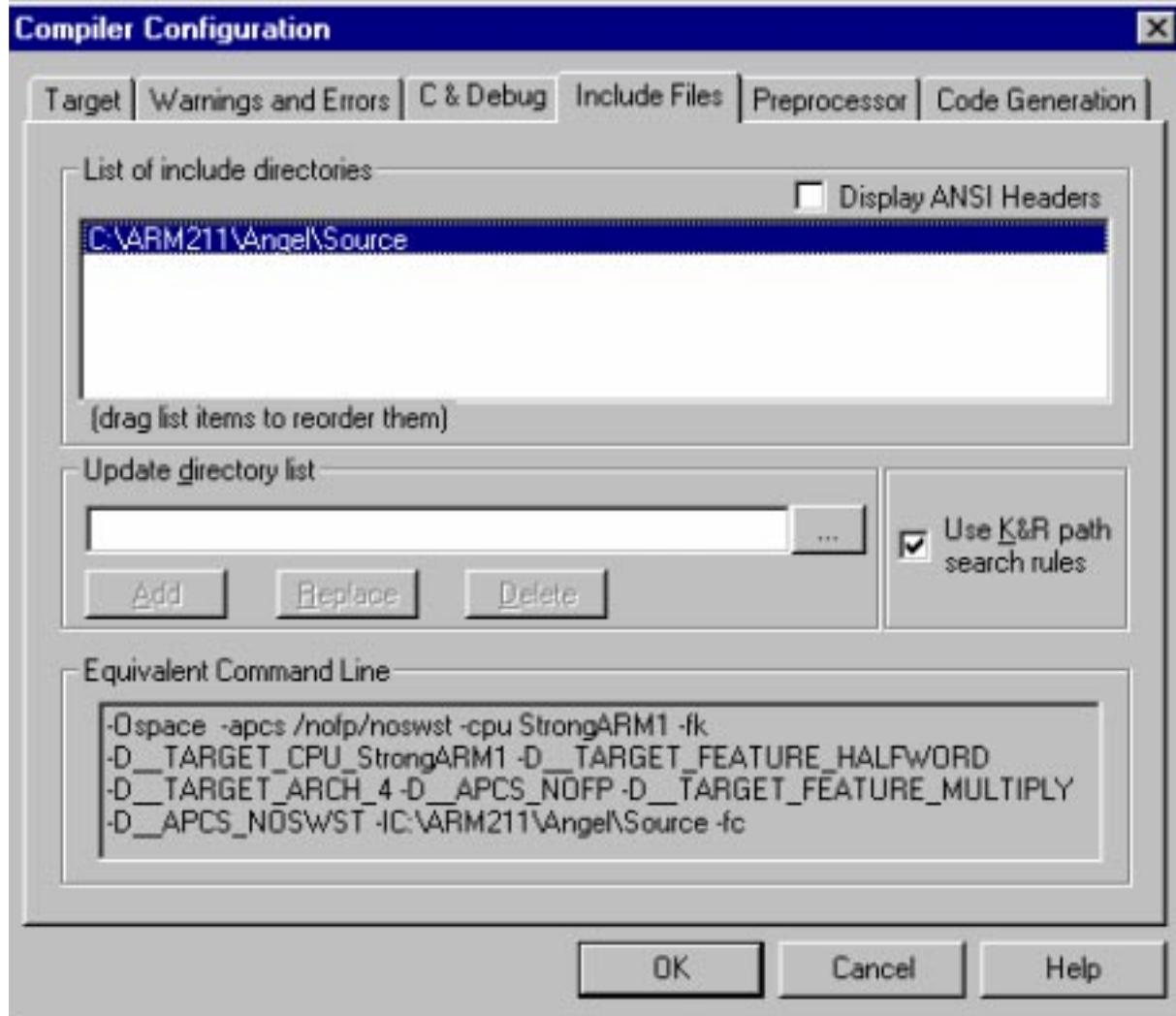
Equivalent Command Line

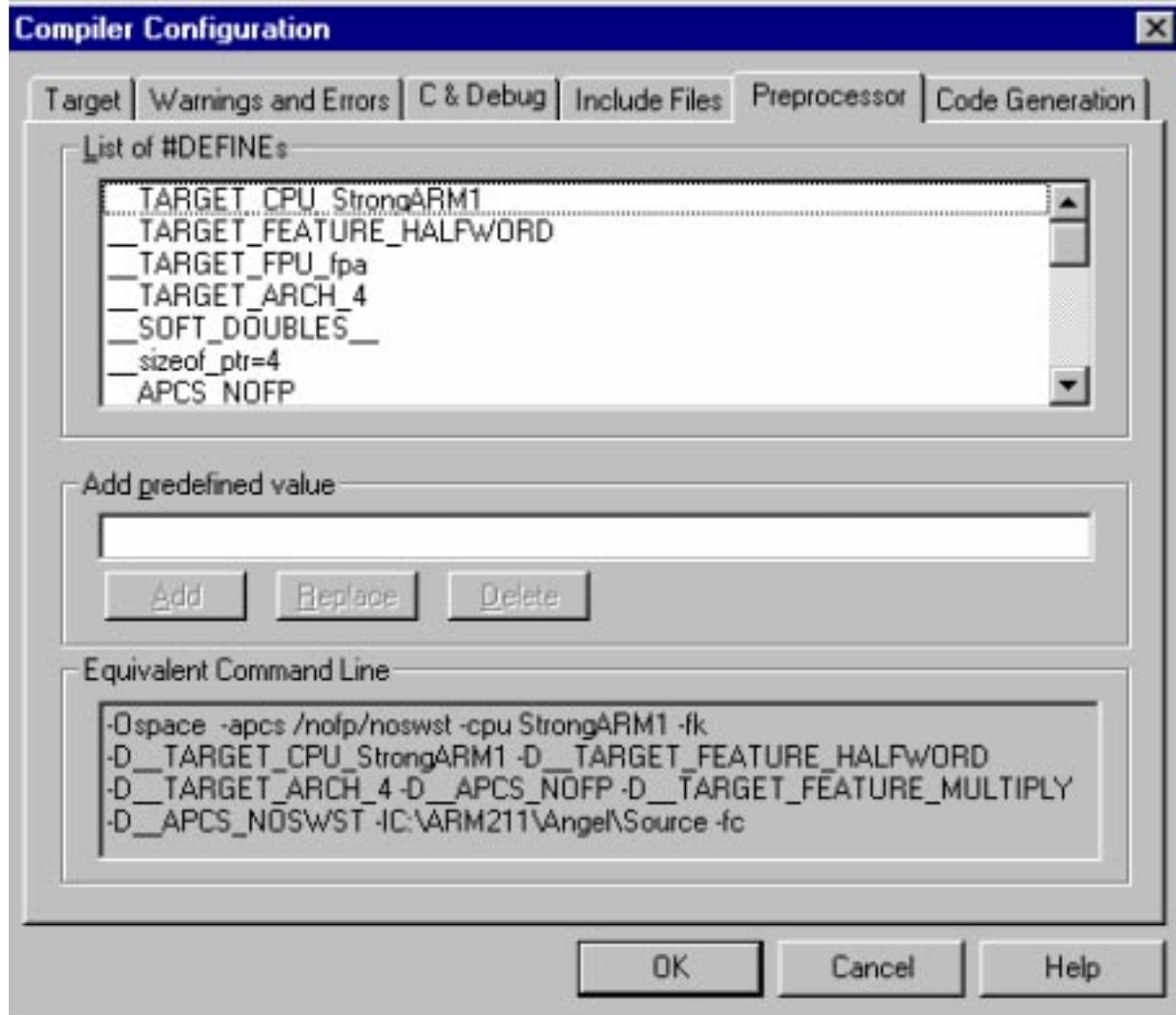
```
-Ospace -apcs /nopfp/noswst -cpu StrongARM1 -fk  
-D__TARGET_CPU_StrongARM1 -D__TARGET_FEATURE_HALFWORD  
-D__TARGET_ARCH_4 -D__APCS_NOPFP -D__TARGET_FEATURE_MULTIPLY  
-D__APCS_NOSWST -IC:\ARM211\Angel\Source -fc
```

OK

Cancel

Help





Compiler Configuration

Target | Warnings and Errors | C & Debug | Include Files | Preprocessor | Code Generation

Addressing Mode

- 32 bit
 26 bit

Processor

StrongARM1

Architecture

- 3 4
 3I 4I

Floating point emulator

- 2 compatibility
 3 compatibility

Byte Sex

- Little Endian
 Big Endian

Floating point

- Software library
 Hardware

APCS3 Qualifiers

- Reentrant
 Software stack check
 Frame pointer
 F P arguments passed in FP Registers
 Arm/Thumb interworking

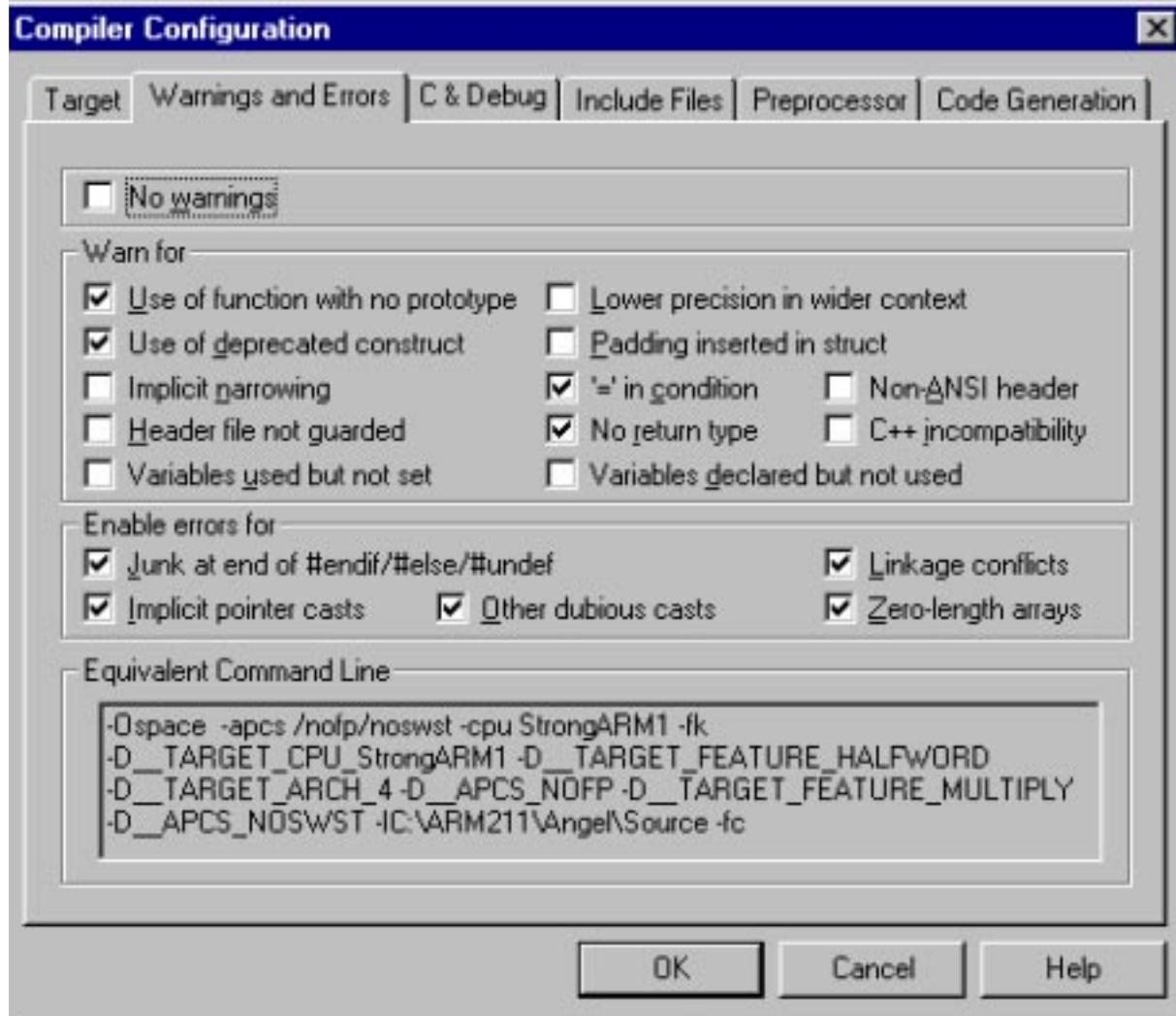
Equivalent Command Line

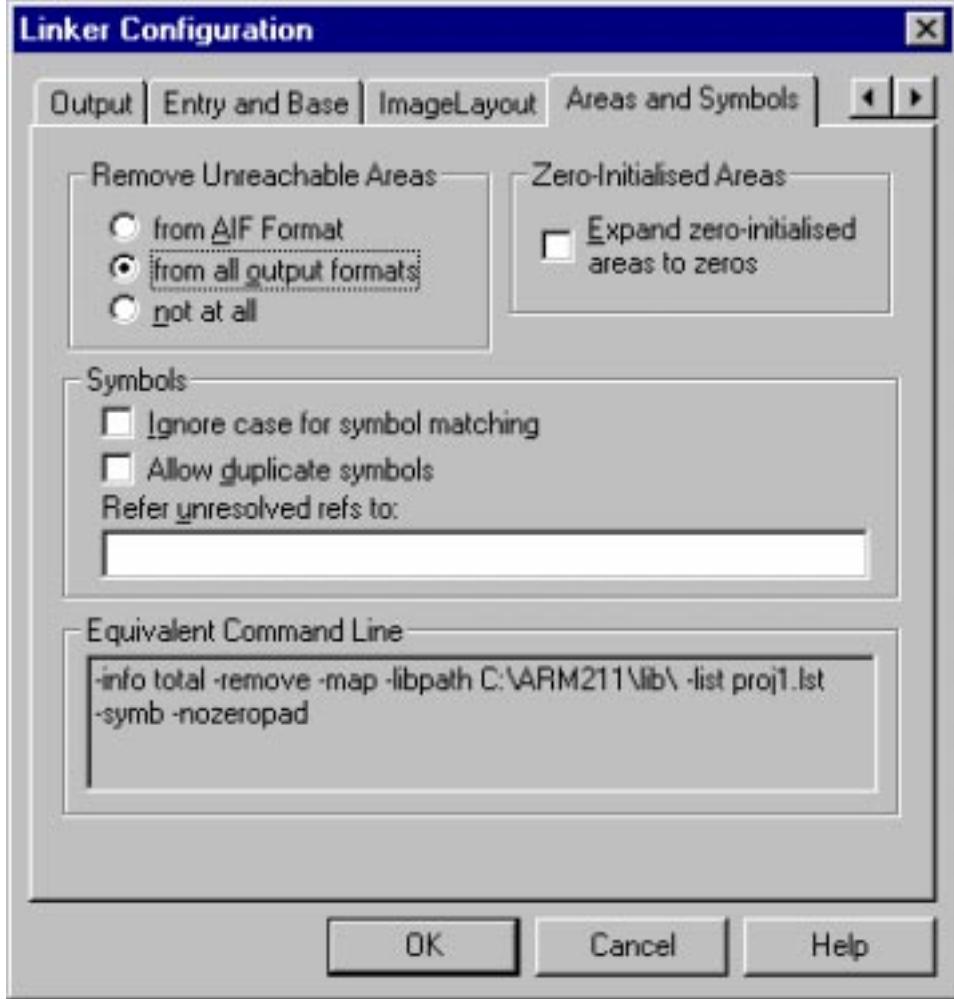
```
-Ospace -apcs /nopfp/noswst -cpu StrongARM1 -fk  
-D__TARGET_CPU_StrongARM1 -D__TARGET_FEATURE_HALFWORD  
-D__TARGET_ARCH_4 -D__APCS_NOPFP -D__TARGET_FEATURE_MULTIPLY  
-D__APCS_NOSWST -IC:\ARM211\Angel\Source -fc
```

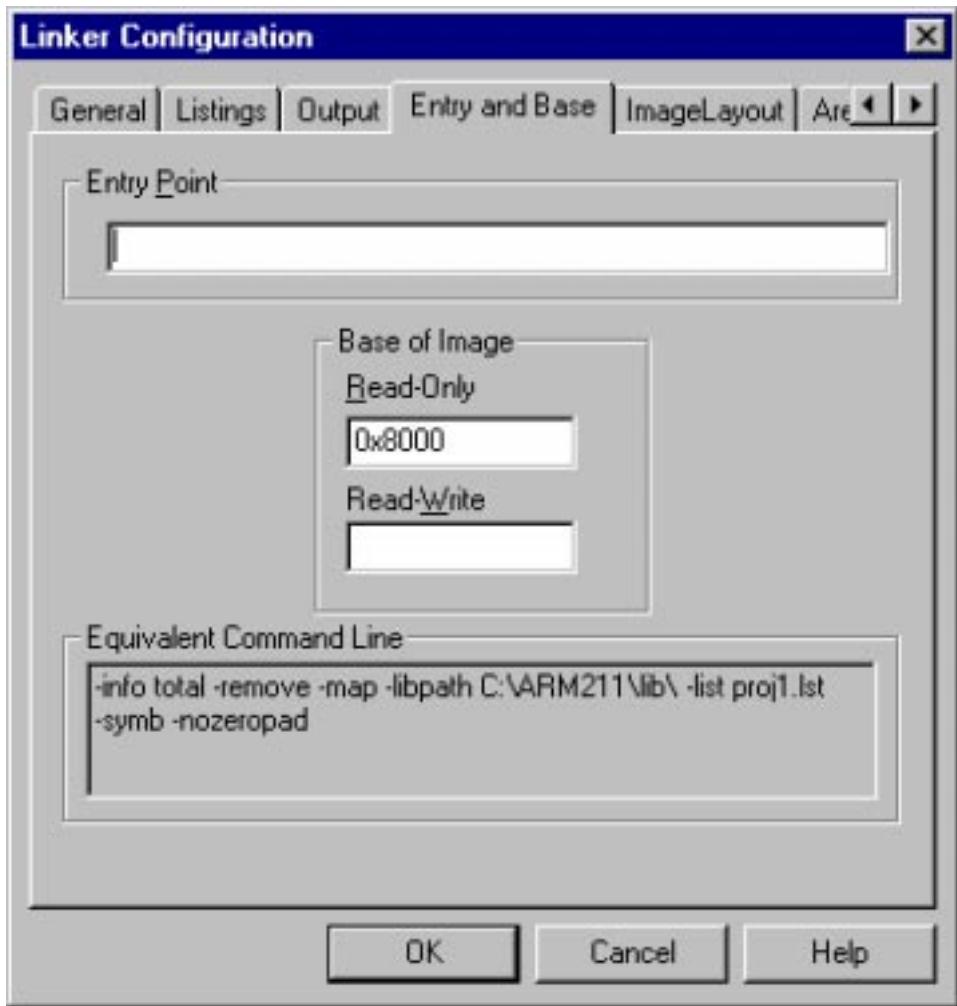
OK

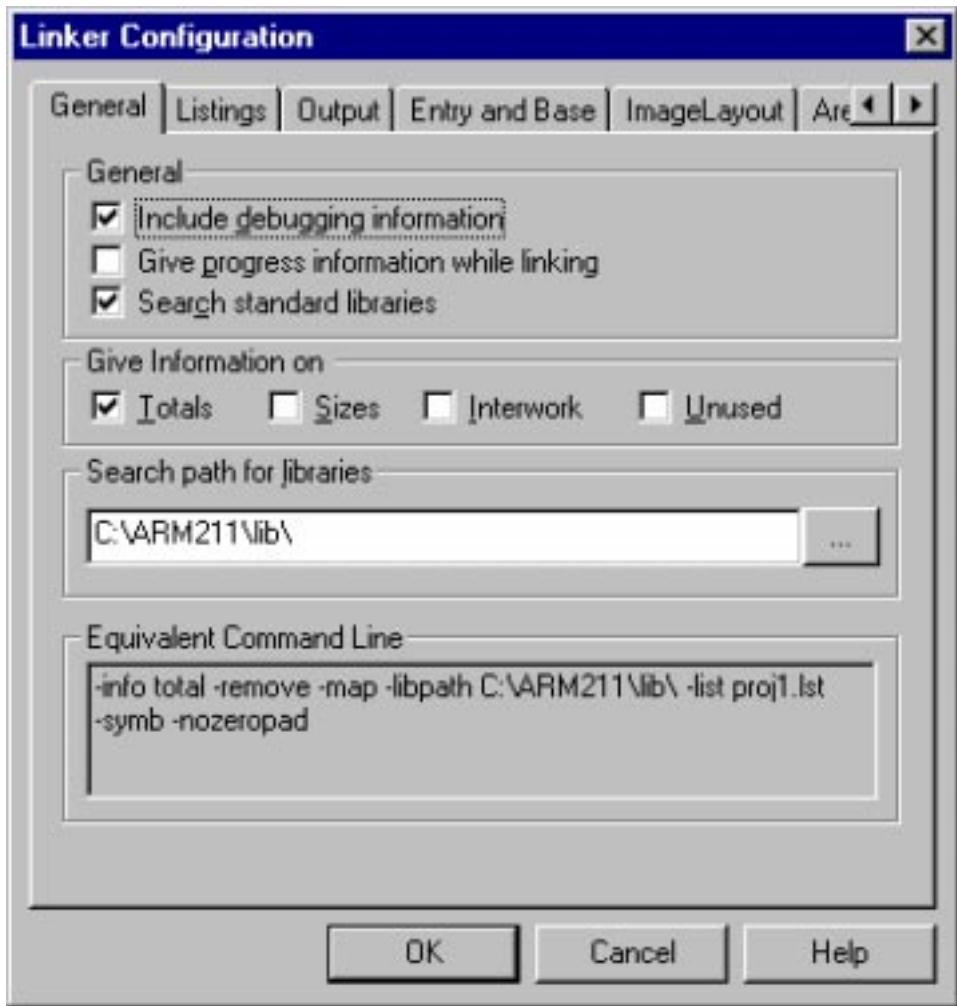
Cancel

Help



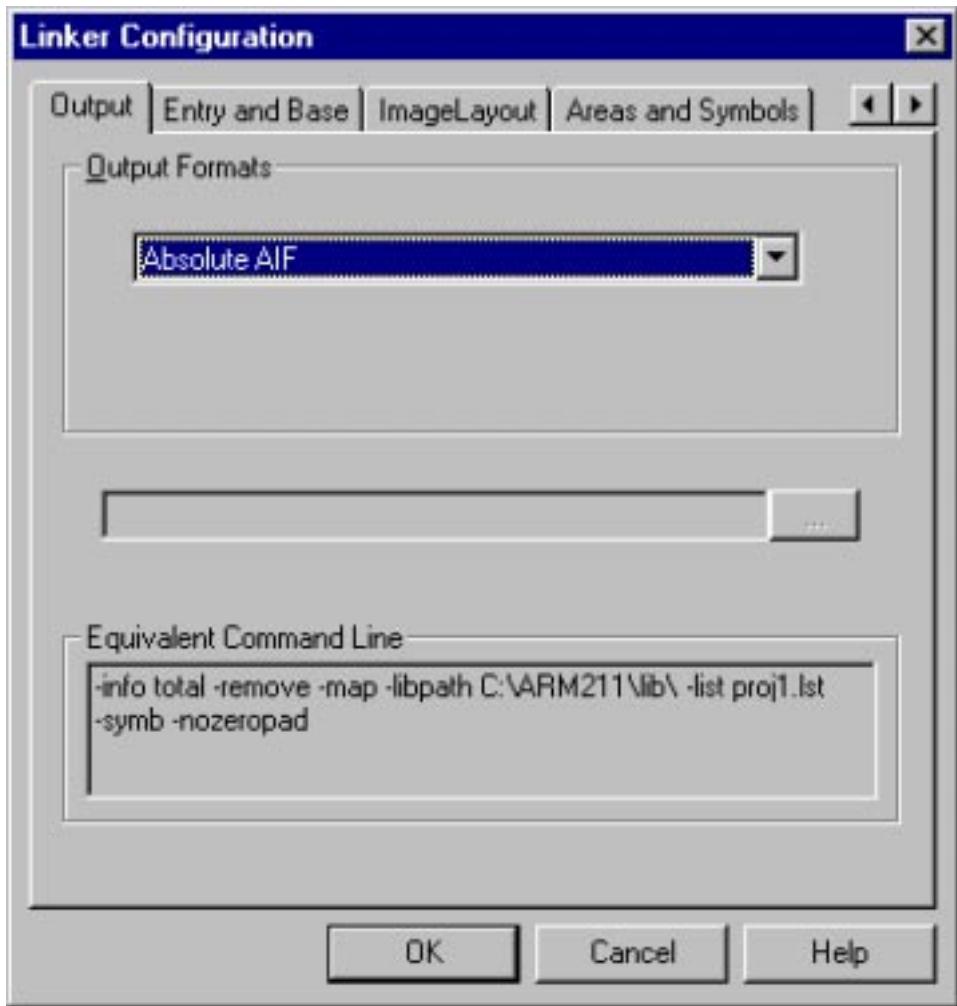




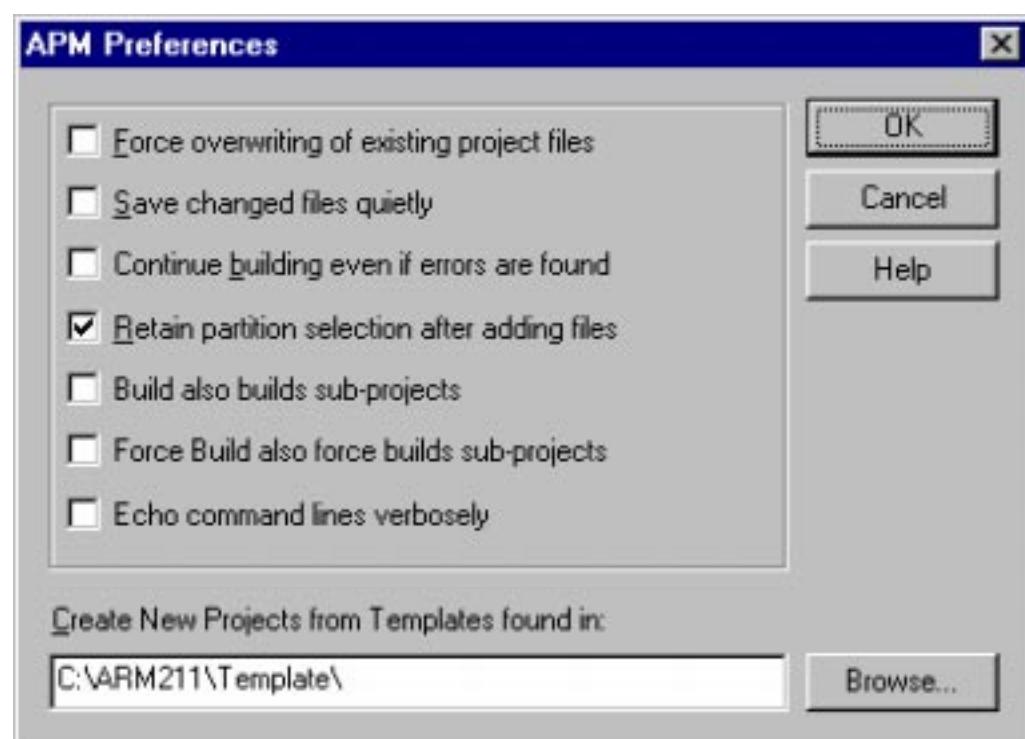


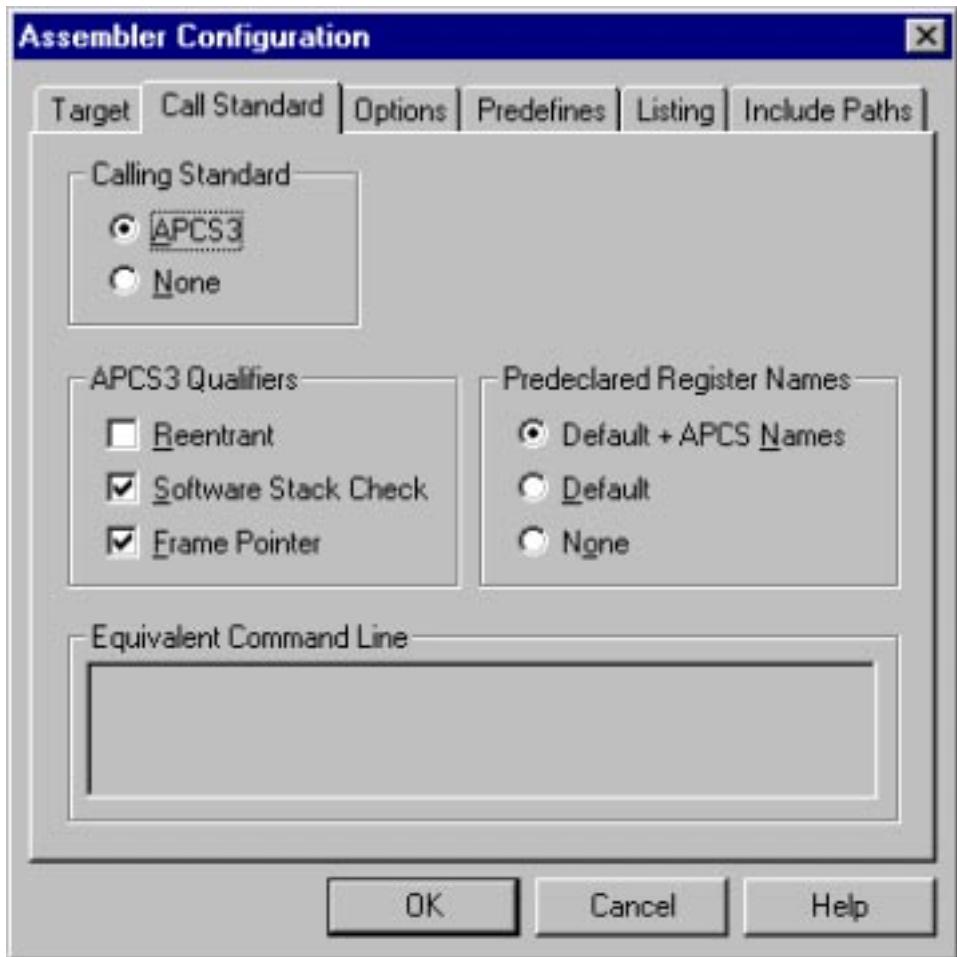




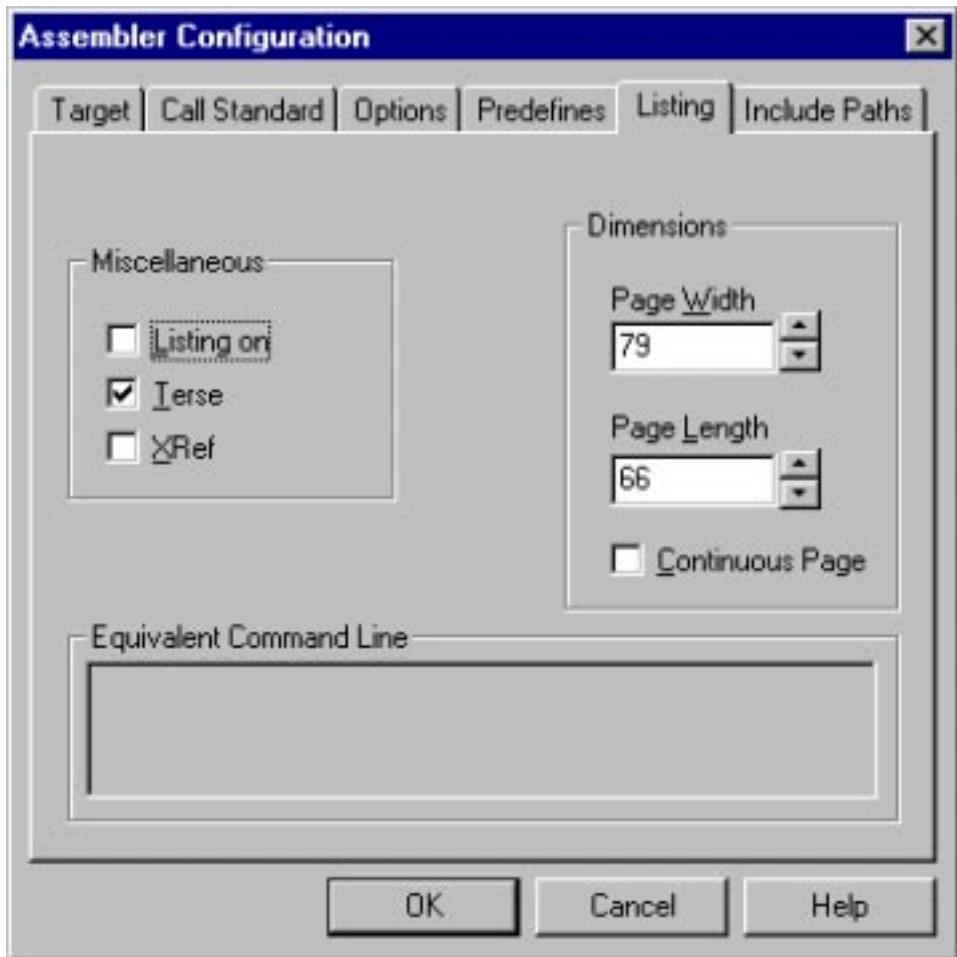


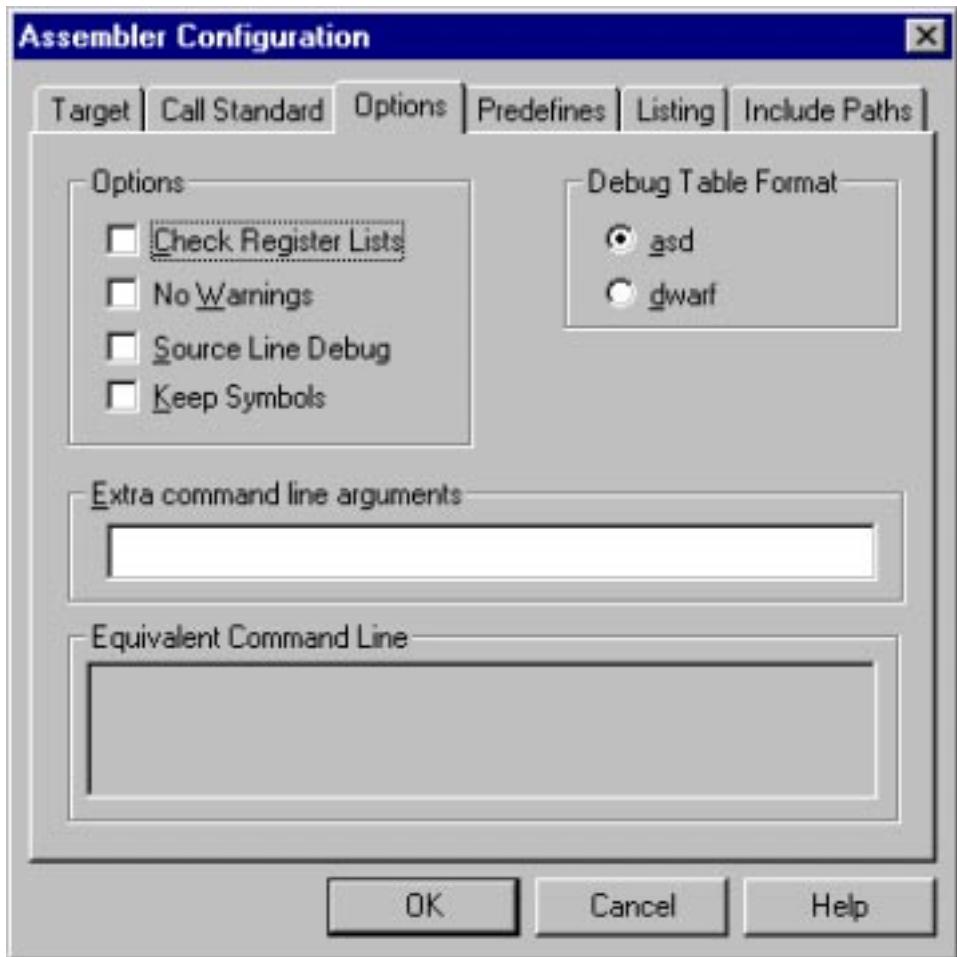
Generic Settings for all tools

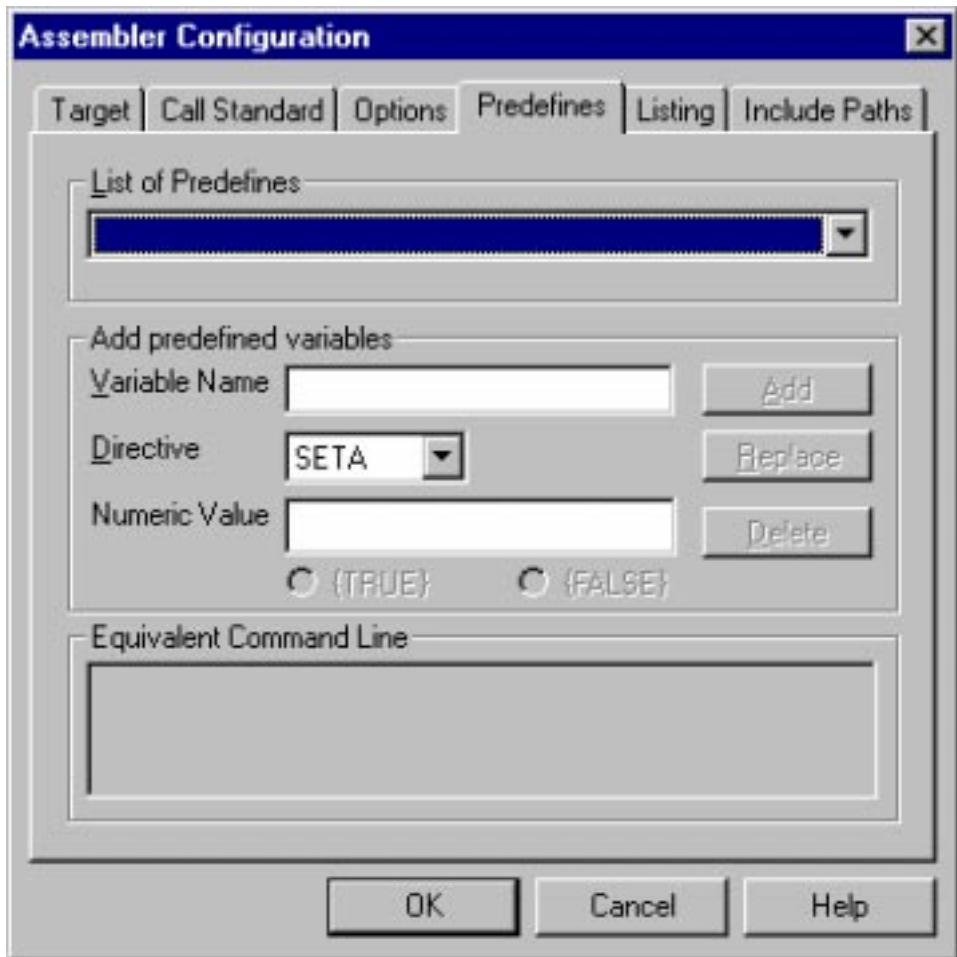


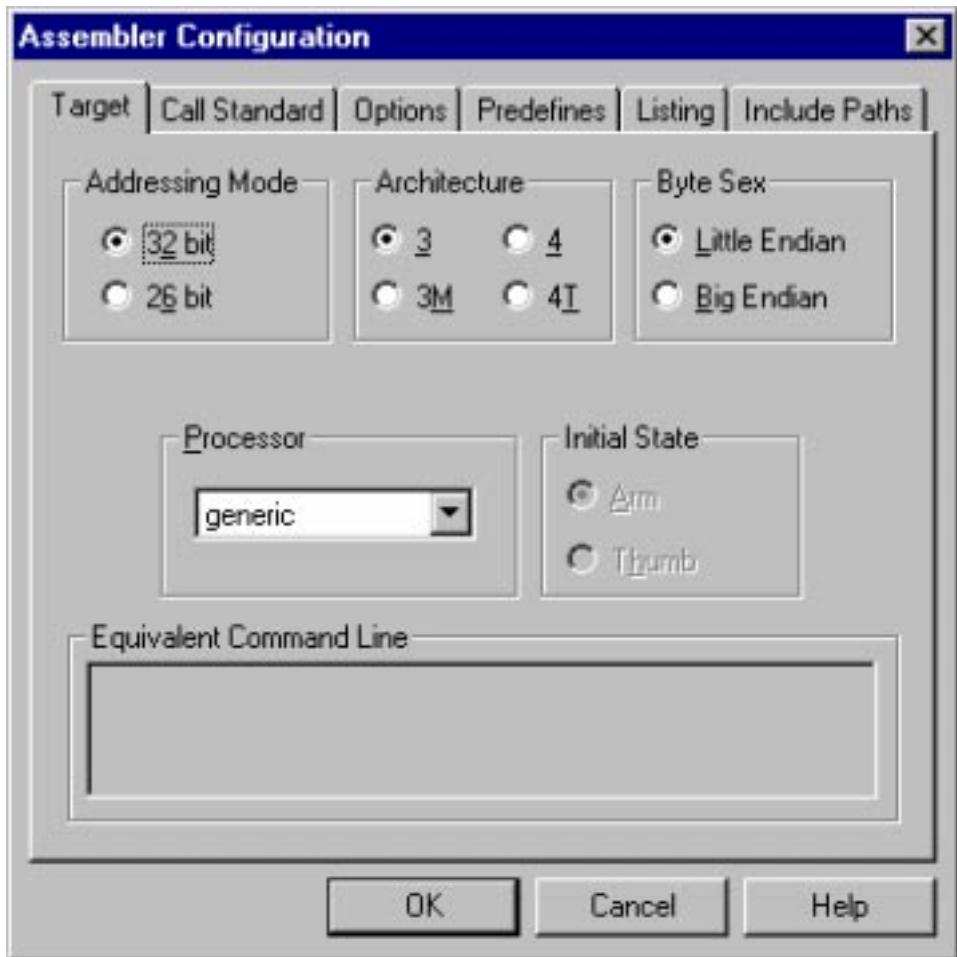


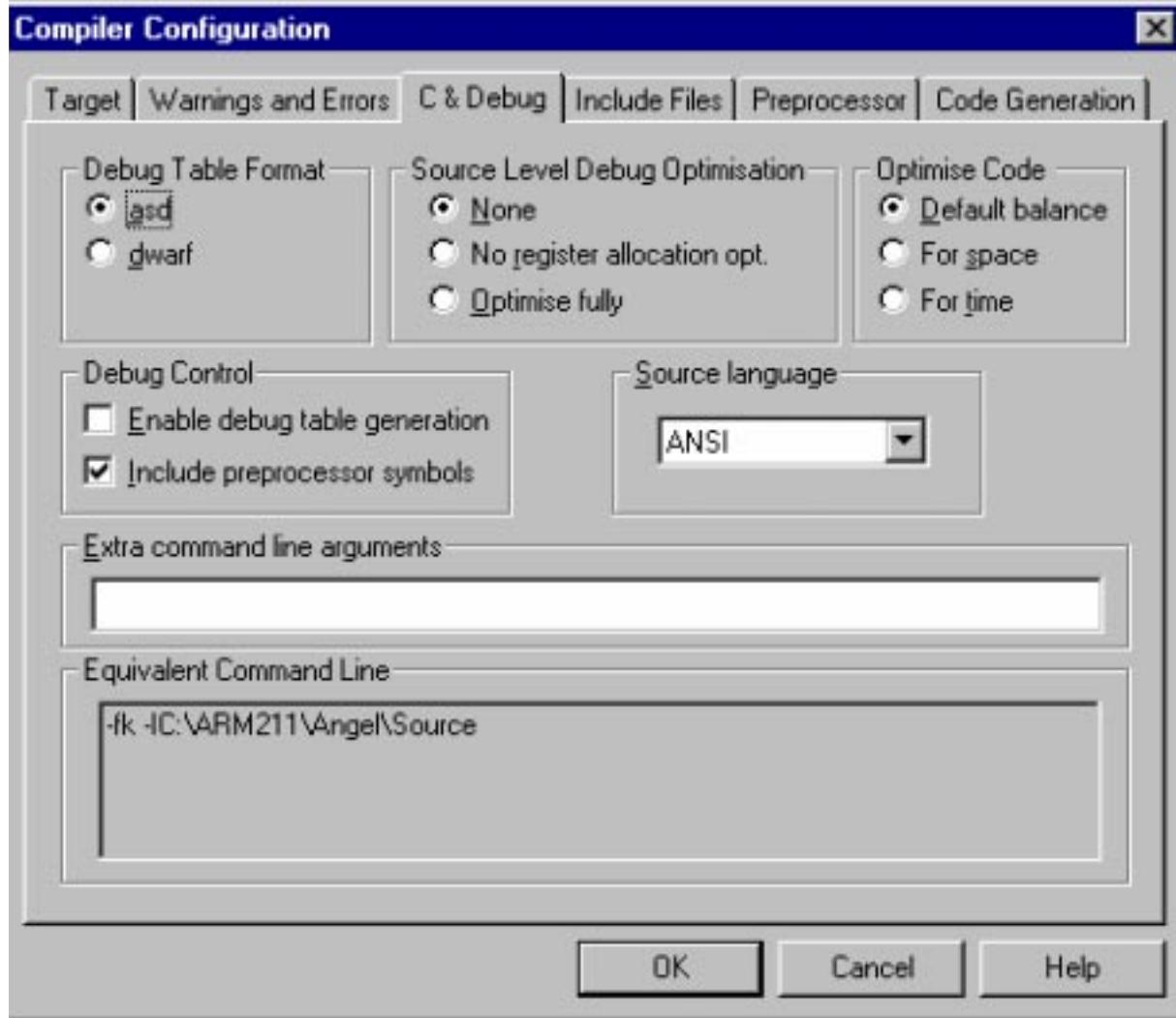


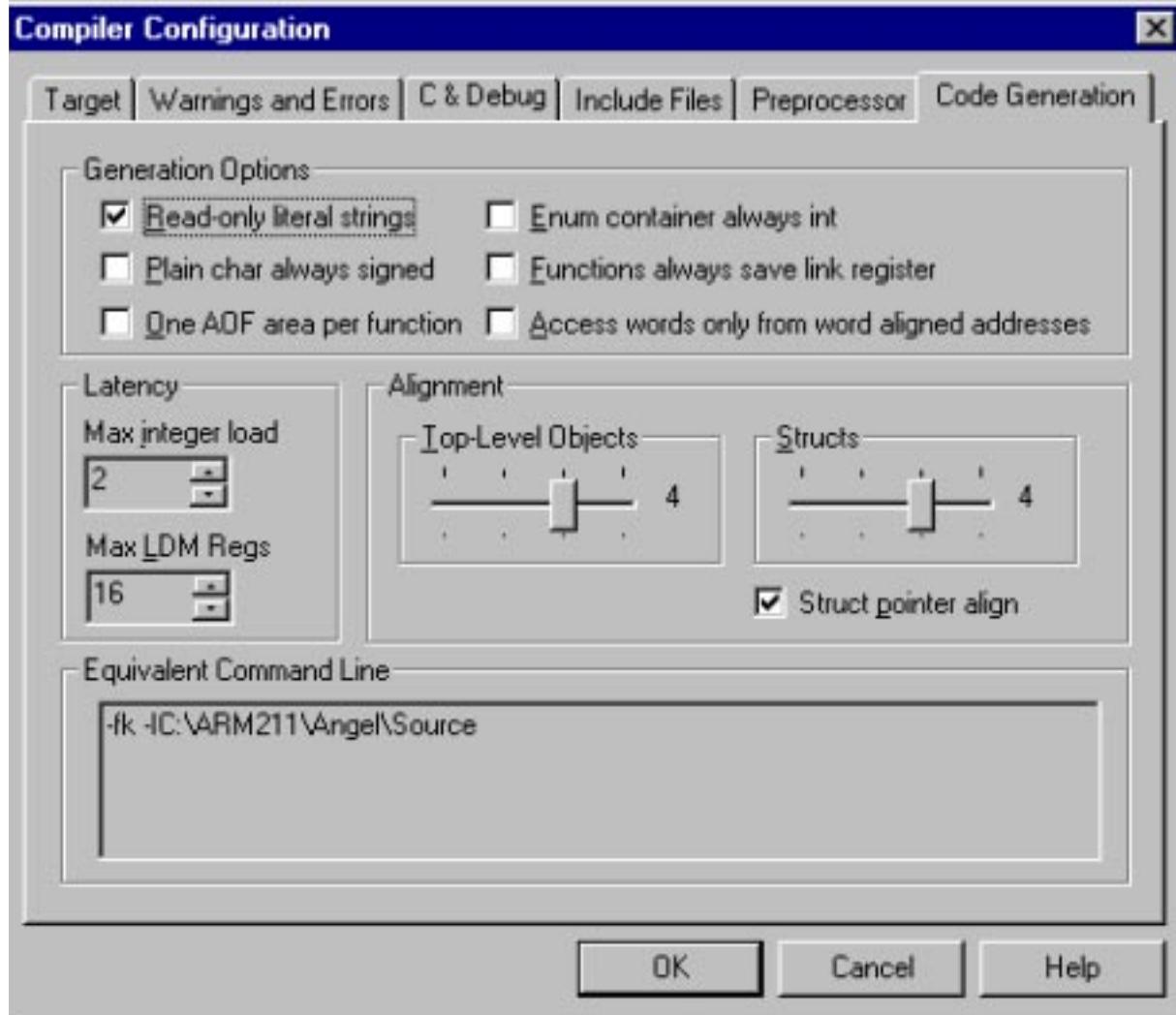


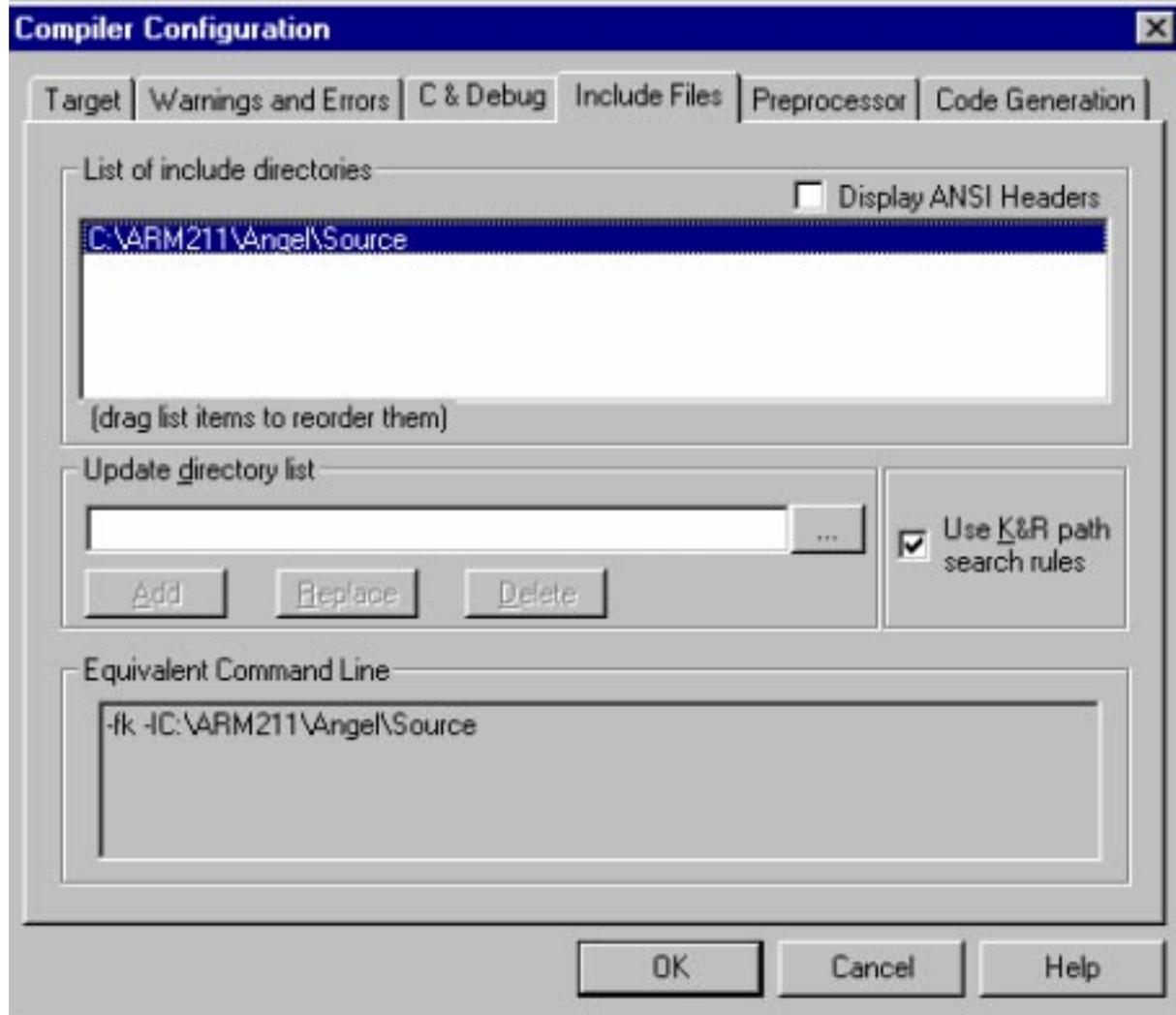












Compiler Configuration

Target | Warnings and Errors | C & Debug | Include Files | Preprocessor | Code Generation |

List of #DEFINES

```
TARGET_FPU_fpa  
SOFT_DOUBLES  
TARGET_ARCH_3  
sizeof_ptr=4  
sizeof_long=4  
CC_NORCROFT=1  
arm
```

Add predefined value

Equivalent Command Line

```
-fk -IC:\ARM211\Angel\Source
```

