

# Hoang Thach Nguyen

Post-doc, Automatic Control Group, KTH School of Electrical Engineering.

## Contact

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## Personal

Birth: November 3, 1984.  
Citizenship: Vietnamese.

## Professional experience

February 2013 - February 2014: Post-doc, Automatic Control Lab, KTH School of Electrical Engineering.  
May 2012 - August 2012: Post-doc, ISN group, Imperial College.

## Education

2008 - May 2012: Ph.D. Computer Science, LIAFA, Université Paris 7.  
*Advisor:* Jean Mairesse.  
2007-2008: M.Sc. in Computer Science, École Polytechnique.  
2007-2008: Télécom-ParisTech (formerly École Nationale Supérieure des Télécommunications),  
Major in Computer Science.  
2004-2007: École Polytechnique, Major in Computer Science.

## Competences

Languages: English, French, Vietnamese (mother tongue).  
Computer skills: C++, C, Java, L<sup>A</sup>T<sub>E</sub>X, Linux (Gentoo), FreeBSD.

## Research Interests

Petri nets, queueing networks, Markov processes, multi-armed bandits.

## Publications

### *Refereed journals*

“Synthesis and Analysis of Product-form Petri Nets” (with S. Haddad and J. Mairesse). *Fundamenta Informaticae*, to appear.  
“Deficiency Zero Petri Nets and Product-form” (with J. Mairesse). *Fundamenta Informaticae*, vol. 105, n. 3, p. 237-261, 2010.

### *Conferences with refereed proceedings*

“Synthesis and Analysis of Product-form Petri Nets” (with S. Haddad and J. Mairesse). In *Petri Nets 2011*, LNCS, Springer-Verlag, 2011.

“Deficiency Zero Petri Nets and Product-form” (with J. Mairesse). In *Petri Nets 2009*, LNCS 5606, p. 103-122, Springer-Verlag, 2009.

### *Ph.D. thesis*

“Réseaux de Petri stochastiques à forme produit” (in French). Defended on 18/04/2012.

### **Award**

*Outstanding paper award at Petri Nets 2011.*

### **Presentations**

Synthesis and Analysis of Product-form Petri Nets

Réunion du GdR RdP, LIPN, Paris, May 6, 2011.

Deficiency Zero Petri Nets and Product-form

TREC’s seminar, INRIA, Paris, November 4, 2010.

Séminaire 68NQRT, IRISA, Rennes, July 2, 2009.

30th International Conference on Applications and Theory of Petri Nets and Other Models of Concurrency, Paris, Juin 25, 2009.

### **Teaching**

#### *Teaching assistant at Université Paris 7*

Fall 2011: POSIX, Java Programming (project), Java Programming.

Spring 2011: Finite Automata.

Spring 2010: Finite Automata.

Fall 2008: Java Programming.