Bounded Invariant Checking for Stateflow Programs

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Abstract

Stateflow models are complex software models, often used as part of safety-critical software solutions designed with Matlab Simulink. They incorporate design principles that are typically very hard to verify formally. In particular, the standard exhaustive formal verification techniques are unlikely to scale well for the complex designs that are developed in industry. Furthermore, the Stateflow language lacks a formal semantics, which additionally hinders the formal analysis.

To address these challenges, we lay here the foundations of a scalable technique for provably correct formal analysis of Stateflow models, with respect to invariant properties, based on bounded model checking (BMC) over symbolic executions. The crux of our technique is: i) a representation of the state space of Stateflow models as a symbolic transition system (STS) over the symbolic configurations of the model, as the basis for BMC, and ii) application of incremental BMC, to generate verification results after each unrolling of the next-state relation of the transition system. To this end, we develop a symbolic structural operational semantics (SSOS) for Stateflow, starting from an existing structural operational semantics (SOS), and show the preservation of invariant properties between the two. Next, we define bounded invariant checking for STS over symbolic configurations as a satisfiability problem. We develop an automated procedure for generating the initial and next-state predicates of the STS, and propose an encoding scheme of the bounded invariant checking problem as a set of constraints, ready for automated analysis with standard, off-theshelf satisfiability solvers. Finally, we present preliminary performance results by applying our tool on an illustrative example.

1 Introduction

Stateflow [1] is a proprietary graphical modelling language developed and maintained by Mathworks. It is an extension of a formalism for modelling complex

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systems through hierarchical state machines called Statecharts [2]. The expansion and popularity of the Stateflow language is mostly due to its integration into the Matlab Simulink graphical development and simulation environment [3], which makes it a standard tool for modeling (hierarchical) state-machines in many industrial domains, such as the automotive, avionics, and railway domains. The rich graphical formalism and the variety of supporting tools in the Matlab Simulink environment enable the development of highly complex software models, which in many instances are classified as safety-critical. The correctness of safety critical systems is regulated by domain-specific safety standards (e.g., ISO26262 [4] in the automotive domain), which require correct operation of such systems at all times with strongly regulated error margins.

One way of enabling a high level of quality-assurance for safety-critical systems is to employ rigorous mathematics-based verification methods, popularly known as formal verification techniques. The starting point of applying such techniques is the existence of a precise formal model of the system design. To be able to generate such a model, besides a formal syntax, the modeling language in which the system design is produced must have a precisely defined execution semantics. However, even in its most comprehensive documentation [1], the execution semantics of the Stateflow language has only been described informally. Still, Mathworks offers a tool, namely the Simulink Design Verifier (SLDV) [1], which provides features for test case generation and formal property checking. The SLDV tool is of a closed-source, proprietary nature, and not much information about the employed formal verification technique has been disclosed so far, except that it implements SAT-based model checking [5]. According to Etienne et al. [6], SLDV implements SAT-based bounded model checking and k-induction using the Prover SL DE tool [7], yet little information is presented as to how exactly the original Stateflow models are analysed. On top of the information scarcity, the SLDV tool is distributed under a license that explicitly forbids benchmarking or any other form of direct comparison with another approach or tool, be it commercial or of purely academic nature.

The problem of formal verification of Stateflow models has been addressed in a number of research endeavours, which have focused either on defining a formal semantics for the language [8–11], aimed to be used as the basis for formal analysis, or propose a model-to-model transformation scheme of Stateflow models into some formal analysis framework, without having an underpinning formal semantics for Stateflow itself [12-14]. The Stateflow verification approaches predominantly focus on exhaustive techniques, in order to determine whether a Stateflow model satisfies given property or not. However, due to the complex nature of the Stateflow models, particularly in industrial settings, generating a formal correctness certificate is often intractable, mostly due to the state-space explosion problem [15, p. 77]. In such cases, the verification task runs out of physical memory and fails, without providing much useful feedback to the designers. Apart from this scalability limitation, some of the approaches for model-to-model transformation (see e.g. [13]) lack the means to formally demonstrate the correctness of the formal analysis model w.r.t. the original Stateflow model, and consequently of the obtained analysis results.

In this work, we are tackling the aforementioned challenges for formal analysis of Stateflow models by presenting a technique that applies bounded model checking (BMC) [16] over symbolic executions [17] of Stateflow models. We adopt BMC as the underlying technique for verification for two main reasons: first, to leverage the power of SAT/SMT-based model checking [18], and second, to alleviate the state-space explosion by incrementally exploring all system executions of bounded length [18], until the problem becomes intractable. In this paper, we focus on checking invariant properties, which are state properties that hold in all reachable states of a given program. Even though invariant properties represent just one class of properties, based on our previous and current experiences in collaboration with industrial partners, it is often considered to be the most important one for safety-critical systems.

There are two crucial aspects of our verification technique. Fist, we develop a provably correct transformation of Stateflow models into *symbolic transition* systems (STS), and second, we encode the invariant checking problem for STS as a quantifier-free SMT problem that can be checked using any modern SMT solver.

Contributions Our verification technique consists of the following ingredients. First, we derive a set of symbolic structural operational semantics rules (SSOS). The SSOS rules are obtained by uniformly translating into symbolic counterparts the rules of an already existing SOS for Stateflow [8]. We build on top of this particular set of SOS rules, because it is the only available operational semantics for Stateflow that is suitable for our needs, and because the correctness of the rules has already been validated against the *simulation* semantics of Stateflow (see [8]). The SSOS is needed for deriving STS at a suitably high level of granularity of the execution steps (which we choose to be the level of Stateflow program statements), abstracting from the intricate many-layered transitions of the original SOS. As our second contribution, we present two theorems that show that the SOS and SSOS simulate each other. This result is crucial for the correctness of our technique. Our third contribution is a translation, using the SSOS, of Stateflow programs into STS over symbolic configurations, and the encoding of the STS and the given invariant property into a set of constraints in the SMT-LIB format [19]. The latter set of constraints can then be used as input to most of the modern SMT solvers. In our work, we use the Z3 SMT solver [20] from Microsoft Research. Finally, we present preliminary evidence for the practical usefulness of our approach, by applying it on an illustrative Stateflow model. Even though initially we planned to compare our approach against the SLDV tool, in the end it was not possible due to the strict licensing constraints imposed by Mathworks.

Structure Our paper is organised as follows. In Section 2 we outline the required background concepts that we use throughout the paper, including a brief overview of the Stateflow modelling language illustrated on a running example (Section 2.1), the imperative Stateflow language and its formal execution

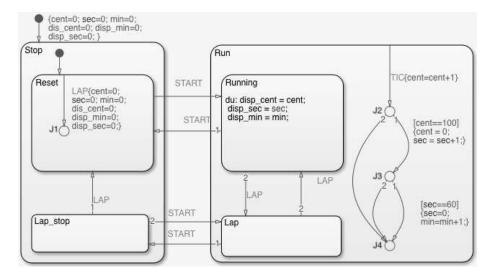


Figure 1: Simple Stateflow diagram - timer example [8].

semantics (Section 2.2), and an overview of the SMT (Section 2.3) and BMC (Section 2.4) techniques. In Section 3 we present the SSOS for Stateflow programs, followed by the characterization of the relationship between the concrete and symbolic semantics in Section 4. Next, in Section 5, we show how an STS over symbolic configurations can be constructed using the SSOS rules (Section 5.1), followed by an informal encoding procedure into an SMT-LIB script (Section 5.2). Next, we show a preliminary evaluation of our approach, based on the running example (Section 6). We give an overview of the related work in Section 7, and finally, in Section 8, present our conclusions and outline some directions for future work.

2 Background

In this section, we present an overview of the concepts on which we build our work. First, in Section 2.1 we give a succinct overview of the Stateflow modeling language. Next, in Section 2.2 we give a brief overview of the existing Stateflow imperative language and its SOS. In Section 2.3 we recall the general concept of Satisfiability Modulo Theories (SMT) and the Z3 tool, and finally in Section 2.4 we give an overview of Bounded Model Checking (BMC).

2.1 Stateflow

Stateflow [1] is a graphical modeling language developed by Mathworks, integrated into the Matlab Simulink [3] modelling environment. The language provides means for modelling of hierarchical state machines through the following features: state transition diagrams, flowcharts, state transition tables,

and truth tables. In our work, we focus on the state transition diagrams and flowcharts.

A Simulink Stateflow diagram can be broadly divided into two parts: control and data. The control part is modeled through the concepts of Stateflow state, connective junction, and transition, whereas the data part is modelled through a set of data variables and events.

The control of the Stateflow diagram in Figure 1 consists of 6 Stateflow states, 4 connective junctions and 13 transitions. We have chosen the particular model as a running example because it has relatively simple control flow that helps us to illustrate our concepts, yet it yields an infinite state-space due to the unbounded integer variable min. Each Stateflow state is decorated with a set of state actions. The basic set of state actions (denoted as A) includes: entry (en), duration (du) and exit (ex). Each action represents an atomic routine. A Stateflow state is either atomic or composite. Composite Stateflow states contain other states (called *substates*) in their internal structure. Based on how their substates are composed, the composite states can be either Or or And. In Or-compositions there can be only one active substate, whereas in And-compositions, all of the substates are active in the same time. parallelism in the context of And-compositions only means concurrent activation of its substates; the execution, however, is strictly sequential, according to a predefined order. Connective junction (or simply junction for short), is used for modelling different branches of execution when a Stateflow diagram moves from one control point into another. The junctions are represented as hollow circles (J1 to J4 in Figure 1).

The dynamics of the control flow of a Stateflow diagram is modelled through a set of transitions of the following format: $s \xrightarrow{e,c,ca,ta} s'$, where s and s' are the source and the destination state or junction, respectively, e is the transition event upon which a transition fires (we use the term "fires" as a synonym for executing the transition), the firing of a transition is enabled by its transition condition c; ca is the conditional action that is executed if the condition evaluates to true and before the transition execution is completed, and finally ta is a transition action that is performed after the transition execution is completed. Transitions can be either full transitions (both source and destination are states) or transition segments (at least source or destination are junction). The control-flow of a Stateflow program evolves only through completion of full transitions. Transitions can be grouped in sets called transition lists, based on a common source element. The internal execution of a transition list is strictly sequential following the order assigned at design-time. There are two types of transition lists: internal and outgoing, with the former being characteristic for states only, whereas the later one for both states and junctions. The action language is separate from the Stateflow itself, and the actions can be specified in the following languages: C, C++, Fortran, or Matlab.

The informal execution semantics of Stateflow models is very intricate and has been explained in detail in the Stateflow user guide published by Mathworks [1]. We omit here the details of the informal execution semantics, but

give in the following section an overview of a formal one.

2.2 Stateflow Imperative Language: Formal Syntax and Structural Operational Semantics

Table 1: The Stateflow Imperative Language by Hamon and Rushby.

```
composition
                                    C = Or(s_a, p, T, SD) \mid And(b, SD)
state definition
                                    sd = ((a, a, a), C, T_i, T_o, J)
                                    SD = \{s_0 : sd_0, \dots, s_n : sd_n\}
state definition list
                                    J = \{j_0 : T_0, \dots, j_n : T_n\}
junction definition list
                                    T = \emptyset_T \mid t.T
transition list
transition
                                    t = (e_t, c, a, a, d)
                                     active state
                                                                s_a = \emptyset_s \,|\, s
state
junction
                                                                d = p \mid je_t = \emptyset_e \mid e
                                    destination
path
              p = \emptyset_p \mid p
event
                                    transition event
action
                                    condition
```

In order to formalize Stateflow, Hamon and Rushby propose in [8,9] an imperative language that is a strict subset of the Stateflow graphical language. To the best of our knowledge, the imperative language supports most of the graphical language features except for the history nodes, and as such is expressive enough for most of the industrial Stateflow models. A *Stateflow program* is simply a Stateflow model (re)written in the imperative language. The execution of Stateflow programs is formalised by a set of 27 rules presented in a structural operational semantics (SOS) style. In the following, we give a brief overview of the language and its operational semantics.

The syntax of the Stateflow imperative language is given in Table 1. The language is based on the following syntactic categories: state (s), junction (j), event (e), action (a) and condition (c). A transition $t = (e_t, c, a_c, a_t, d)$ is composed of a transition event e_t , condition c, condition and transition actions a_c , a_t , respectively, and a destination d to which it fires. Transitions are grouped into transition lists. A junction definition list J associates a list of transitions with junctions. A state definition list SD associates each state variable (s) with a state definition $sd = ((a, a, a), C, T_i, T_o, J)$. Each sd contains 3 actions, a composition C, lists of internal and outgoing transitions T_i and T_o , respectively, and a junction definition list J. Finally, the composition C can be of type $Or(s_a, p, T, SD)$, where s_a is the active state, p is the path, T is a transition list, and SD is a list of state definitions; or of type And(b, SD), which has a Boolean value b signifying whether the component is active or not, a path p, and a state definition list SD. In the reminder of the manuscript, we will use the term Stateflow program regardless if it is modeled using the original Stateflow graphical language or the imperative language as we are going to be handling

$$\frac{(e = e_0) \lor (e_0 = \emptyset) \quad e \vdash (c, D_1) \to \top \quad e \vdash (ca, D_1) \hookrightarrow D_2}{e \vdash ((e_0, c, ca, ta, d), D_1) \to D_2, Fire(d, ta)}$$
(a) [t-FIRE]_{SOS} rule [9]

$$(e = e_0) \lor (e_0 = \emptyset)$$

$$e \vdash (c, \langle \Delta_1, pc_1 \rangle) \to \langle \Delta_1, pc_2 \rangle \quad e \vdash (ca, \langle \Delta_1, pc_2 \rangle) \hookrightarrow \langle \Delta_2, pc_2 \rangle$$

$$e \vdash ((e_0, c, ca, ta, d), \langle \Delta_1, pc_1 \rangle) \to \langle \Delta_2, pc_2 \rangle, Fire(d, ta)$$
(b) [t-FIRE]_{SSOS} rule

Figure 2: The [t-FIRE] rule in (a) SOS and (b) SSOS semantics.

only models that can be rewritten in the imperative language.

The execution of a Stateflow program consists of processing an input event through a sequence of discrete steps. The execution involves modifying the value of the program variables, raising output events, and changing the program control point by executing transitions. The operational semantics is formalised by a set of 27 layered rules, which precisely prescribe the sequence of actions involved in the processing of an event through the elements of the imperative language. The operational semantics has been validated against the simulation semantics of the Matlab Stateflow simulation engine by performing extensive simulations with Stateflow graphical models and comparing the output traces with the (SOS based) executions of the corresponding Stateflow programs [8]. In this work, we refer to executions derivable using the SOS rules as concrete. The following judgment expresses the base form of event processing in Stateflow programs [8]:

$$e \vdash (P, D) \rightarrow (P', D'), tv$$

which reads as follows: processing an event e in an environment D through a program component P produces a new environment D', a new program component P' and a transition value tv. An environment $D: Var \to Val$ is a mapping from variables to values. Env denotes the set of all possible environments; P is an element of the Stateflow imperative language, whereas $tv \in \{Fire(d, a) \mid No \mid End\}$ is a transition value which indicates whether a transition has fired (Fire(d, a)) or not $(No \mid End)$. All of the rules in the SOS extend and slightly differ from this general form [9].

The [t-FIRE] rule for both SOS and SSOS semantics is shown in Figure 2a and Figure 2b, respectively. The rule describes how a Stateflow transition fires, and intuitively it captures the following: in the concrete execution, if the evaluation of a condition evaluates to true (\top) , and the execution of the condition action ca modifies the environment, then a Stateflow program performs a transition, and raises a *Fire* transition value. In Figure 2b, we show an SSOS counter-part of the [t-FIRE] rule to visually illustrate the similarities and differences of the rules side-by-side. An intuitive explanation for the rule is given

in Section 3. For the complete set of SOS rules, we refer the interested reader to the original work by Hamon and Rushby [8,9].

2.3 Satisfiability Modulo Theories and Z3

The problem of determining whether a Boolean formula can be made true by assigning truth values to the constituent Boolean variables is known as the *Boolean satisfiability problem* (SAT). A decision procedure for SAT is a procedure that generates a (satisfying) assignment for the variables for which a given formula is true, whenever the formula is satisfiable. *Satisfiability Modulo Theories* (SMT) represents an extension of SAT, where some of the logic symbols are interpreted by a background theory [18]. An example of such a background modulo-theories are the theory equality, theory of integer numbers, theory of real numbers, etc.

Z3 [20] is a state of the art SMT solver and theorem prover developed by Microsoft Research. The input is a model specified in a text-based assertion language that follows the SMT-LIB standard [19]. Z3 provides a number of APIs for different programming languages, including C and Python, which enables the integration of the Z3 solver with other applications. The input model consists of a set of variables of specific types (also called sorts), and a set of assertions that express constraints over the variables. The basic command called assert is used to add an assertion to the internal stack of the solver. Once the stack is loaded with the set of assertions of interest, the satisfiability of the constraints is checked by the check-sat command. There are 3 possible outcomes from the decision procedure: sat which indicates that there exists an assignment of the variables satisfying the set of assertions, unsat indicating that there does not exist such a satisfying assignment, and unknown when the decision procedure cannot determine the satisfaction of the assertions on the stack.

2.4 Bounded Model Checking

In this section, we give an overview of the Bounded Model Checking (BMC) technique, which we use for checking invariant properties of Stateflow programs.

BMC is a refutation-based verification technique, in which a symbolic representation of the system behavior is unrolled for a predefined number k of steps, called the *reachability diameter*. It has been shown that checking a property over a finite set of states can be reduced to checking the satisfiability of a corresponding propositional formula. Thus, the goal of BMC is to generate a formula that is satisfiable if there exists a violation of the property in some execution of length up to k. The reduction of the model-checking problem to a satisfiability problem is motivated by the increase in the computational power of modern solvers, which tend to be more efficient in solving large formulas as compared to techniques based on BDDs [21].

To be able to formally define the BMC problem for invariant properties, we first recall some additional background concepts. A common way of capturing program behavior is via a *transition system* (TS), formally defined as follows.

Definition 1 (Transition System). A transition system is a tuple $TS = (C, C_0, \rightarrow)$, where: C is a finite set of configurations, $C_0 \subseteq C$ a set of initial configurations, and $\rightarrow \subseteq C \times C$ a transition relation.

Let π be a finite or infinite sequence of configurations, written $\pi = c_0, c_1, \ldots, c_n$ or $\pi = c_0, c_1, c_2, \ldots$, respectively. The sequence π is called:

- a path of TS, written $path(\pi)$, if $\forall c_i, c_{i+1} \in \pi$. $(c_i, c_{i+1}) \in \rightarrow$, and
- an initialized path, if $path(\pi)$ and $c_0 \in C_0$.

A finite path $\pi = c_0, c_1, \dots, c_n$ is said to be of length n.

Transition systems are a useful concept for reasoning about the *concrete computations* of programs. A configuration $c \in C$ of a program is a pair c = (l, v), where l is a control point in the program, and v is a mapping between the program variables and values taken from their respective domains.

When programs contain variables that range over infinite domains, it can be more efficient to reason about their behaviors in a *symbolic* way, through a set of predicates. This gives rise to the notion of symbolic transition system (STS).

Definition 2 (Symbolic Transition System). A symbolic transition system is a pair S = (I, R), where the unary predicate $I(\cdot)$ is a first-order logic (FOL) formula over the components of configurations representing the initial set of configurations, and the binary predicate $R(\cdot, \cdot)$ is a formula representing the "next-state" transition relation, satisfying the equivalences:

$$I(c) \Leftrightarrow c \in C_0$$

 $R(c, c') \Leftrightarrow (c, c') \in \rightarrow$

Every initialized path in S of length k can be characterized by the formula:

$$path(c_0, c_1, \dots, c_k) \triangleq I(c_0) \wedge \bigwedge_{i=0}^{k-1} R(c_i, c_{i+1}),$$
 (1)

and then, the existence of an initialized path of length k is equivalent to the satisfiability of the formula $path(x_0, x_1, \ldots, x_k)$, where x_i is a variable representing a configuration.

Since BMC operates over a subset of the reachable configurations, contained within the given reachability diameter k, in this work we use the term k-bounded invariant property to denote an invariant property that holds over the reachability diameter k.

Let φ be a unary predicate over configurations, i.e., a property. We define the corresponding k-bounded invariant property, denoted φ^k , as the formula:

$$\forall c_0, c_1, \dots, c_k. \ (path(c_0, c_1, \dots, c_k) \Rightarrow \bigwedge_{i=0}^k \varphi(c_i))$$
 (2)

To disprove such a k-bounded invariant property, it is sufficient to show that there exists a configuration within the reachability diameter for which φ^k does not hold. A path containing such a configuration is called a *counter-example*, and is characterized by the logical negation of the above formula, i.e.:

$$\exists c_0, c_1, \dots, c_k. \ (path(c_0, c_1, \dots c_k) \land \bigvee_{i=0}^k \neg \varphi(c_i))$$
 (3)

Given that the predicates I, R, and φ can be expressed as FOL formulas, where some function and predicate symbols are potentially interpreted by some background theory, it is obvious how the refutation of k-bounded invariant properties can be reduced to an SMT problem. In case that there exists a satisfying assignment for (3), a counter-example for the invariant property is generated. Conversely, if there exists no such satisfying assignment, the k-bounded invariant property holds.

3 Symbolic Structural Operational Semantics

In this section, we present our SSOS semantics for the Stateflow imperative language, which we use as a basis for constructing an STS \hat{S} for a given Stateflow program. We start from the existing SOS semantics as in [8,9], and transform each of the SOS rules uniformly in a corresponding symbolic counterpart.

From earlier (see Section 2.1), we know that the SOS rules for Stateflow programs are over judgments of the following form:

$$e \vdash (P, D) \rightarrow (P', D'), tv$$

Based on the set of SOS rules, one can induce a $TS = (C, C_0, \rightarrow)$, where C is the set of concrete configurations, each configuration $c \in C$ being a tuple $c = (P, D), C_0 \subseteq C$ is the set of initial configurations, and $\rightarrow \subset C \times C$ is the transition relation, composed of transitions derivable by the SOS rules.

In the original formalization, the sets of variables (Var) and values (Val), as well as the sets of actions (Act) and conditions (Cond) are considered to be a part of the action language which is distinct from the Stateflow language itself. The details for the actions and conditions are abstracted away; however, it is assumed that the semantics of the executing actions and the evaluating conditions is available via judgments of the form:

(i)
$$e \vdash (a, D) \hookrightarrow D'$$
 and (ii) $e \vdash (c, D) \rightarrow \top \mid \bot$

which are read as follows: (i) evaluating an action (a) in a current environment (D) produces a new environment (D'), and (ii) evaluating a condition (c) in an environment (D) produces either true or false Boolean value.

The set of SSOS rules is created by uniformly transforming each of the SOS rules into a corresponding symbolic rule, by: i) replacing each valuation of the program variables, called *environment* (D) with a symbolic representation (Δ) , and ii) adding a path condition (pc). Consequently, we update the action execution and condition evaluation, which evaluate over the symbolic environment and path condition, respectively. Following the basic principles of symbolic execution [17], in the set of SSOS rules we treat the data component of the language in a symbolic way, whereas the control-flow remains concrete.

As we already discussed in Section 2.4, the set of all initialized paths of length k of a Stateflow program can be symbolically encoded as formulas. In such a representation, the analysis operates over sets of environments, rather than with each environment individually. Conceptually, Δ represents a set of concrete environments that is characterized by a path formula that includes all concrete environments reachable at a particular point of execution, while pc represents the path condition that encodes the conditions over data such that Δ is valid.

We define a symbolic configuration $sc \in SC$ as a structure $(P, \langle \Delta, pc \rangle)$, where P is any component from the imperative language from Table 1. We introduce a new set of symbolic variables (symbols), denoted Sym, and a bijection $g: Var \to Sym$ between the program variables and the symbols. The path condition pc is simply a Boolean expression over the set of symbols, whereas the symbolic environment $\Delta \in SEnv$ is a mapping $\Delta: Var \to Expr_{Sym}$ from program variables to (arithmetic) expressions over symbols. Finally, we assume that symbolic action execution and symbolic condition evaluation are provided via semantic functions of type $\mathcal{SA}: Act \to (SEnv \to SEnv)$ and $\mathcal{SB}: Cond \to (SEnv \to BExpr_{Sym})$, respectively.

We can now define the axioms for action execution and condition evaluation, for symbolic execution of Stateflow programs, as follows:

$$e \vdash (a, \langle \Delta_1, pc_1 \rangle) \hookrightarrow \langle \Delta_2, pc_1 \rangle \quad \text{if } \Delta_2 = \mathcal{SA}[\![a]\!](\Delta_1)$$

$$e \vdash (c, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_1, pc_2 \rangle \quad \text{if } pc_2 = pc_1 \land \mathcal{SB}[\![c]\!](\Delta_1)$$

$$(4)$$

The initial symbolic configuration is $(P, \langle pc_0, \Delta_0 \rangle)$, where P is a component of the Stateflow imperative language, $\Delta_0 = g$, and $pc_0 = \top$.

The set of SOS rules can now be uniformly translated into a corresponding SSOS counter-part. In Figure 3, we show a subset of the SSOS rules, in addition to the [t-FIRE] rule from Figure 2b. The [t-FIRE] rule in Figure 2b describes how a Stateflow transition (t) fires by appending the symbolic evaluation of the condition t.c to the current path condition and by symbolically executing the condition action t.ca over the current symbolic environment Δ . When a transition fires, a transition event Fire(t.d, t.ta) is generated. Going back to Figure 3, a transition list T fires via the [T-FIRE] rule when one of its constituent transitions fires. The [SD-FIRE] rule describes how a Stateflow state fires when one of the transitions from the T_o transition list fires. According to the rule, the firing of the transition is followed by symbolic evaluation of the pending action from the existing Fire event, then symbolic execution and

$$\frac{e \vdash (t, \langle \Delta_{1}, pc_{1} \rangle) \rightarrow \langle \Delta_{2}, pc_{2} \rangle, Fire(d, ta)}{e, J \vdash (t.T, \langle \Delta_{1}, pc_{1} \rangle) \rightarrow \langle \Delta_{2}, pc_{2} \rangle, Fire(d, ta)}$$

$$(a) [T-FIRE]_{SSOS} \text{ rule}$$

$$\frac{e, J_{0} \vdash (T_{o}, \langle \Delta_{1}, pc_{1} \rangle) \rightarrow \langle \Delta_{2}, pc_{2} \rangle, Fire(d, ta)}{e \vdash (ta, \langle \Delta_{2}, pc_{2} \rangle) \rightarrow \langle \Delta_{3}, pc_{2} \rangle} e \vdash (C, \langle \Delta_{3}, pc_{2} \rangle) \Downarrow (C', \langle \Delta_{4}, pc_{3} \rangle)}{e \vdash (A.ex, \langle \Delta_{4}, pc_{3} \rangle) \rightarrow \langle \Delta_{5}, pc_{3} \rangle}$$

$$\frac{e \vdash (A, C, T_{i}, T_{o}, J), \langle \Delta_{1}, pc_{1} \rangle) \rightarrow ((A, C', T_{i}, T_{o}, J), \langle \Delta_{5}, pc_{3} \rangle, Fire(d, \diamond))}{e, J_{i} \vdash (And\{s_{0} : sd_{0} \cdots s_{n} : sd_{n}\}, \langle \Delta_{1}, pc_{1} \rangle)}$$

$$\frac{e, J_{i} \vdash (And\{s_{0} : sd_{0} \cdots s_{n} : sd_{n}\}, \langle \Delta_{1}, pc_{1} \rangle)}{\rightarrow (And\{s_{0} : sd'_{0} \cdots s_{n} : sd'_{n}\}, \langle \Delta_{n+1}, pc_{n+1} \rangle, No)}$$

$$(c) [AND]_{SSOS} \text{ rule}$$

$$\frac{(tv = No) \lor (tv = End)}{e, J_{i} \vdash (Sd, \langle \Delta_{1}, pc_{1} \rangle) \rightarrow (sd', \langle \Delta_{2}, pc_{2} \rangle, Fire(p', a))}{\rightarrow (Cr(\emptyset_{s}, p, T, SD[s : sd']), \langle \Delta_{1}, pc_{1} \rangle, tv)}$$

$$\rightarrow (Or(\emptyset_{s}, p, T, SD[s : sd']), \langle \Delta_{2}, pc_{2} \rangle, Fire(p', a))}$$

$$(d) [OR-FIRE]_{SSOS} \text{ rule}$$

Figure 3: Illustrative sample of SSOS rules.

evaluation of the actions and conditions of the currently active inner component(s), and finally the symbolic evaluation of the sd.ex state action. The last two rules, [AND] and [OR-FIRE] capture the correct sequence of elements processing when executing the And and Or-compositions, respectively. Executing an And-composition involves processing each of its constituent states from the state definition list (SD), whereas an Or-composition fires when the underlying sd fires. Due to space limitations, Figure 3 shows only a small fraction of the rules for illustrative purposes. The complete set of 27 SSOS rules is included in Appendix A.

Since we are overloading the transition relation symbol " \rightarrow " in the SOS and SSOS rules, to avoid confusion, further in the paper we shall use " \xrightarrow{SOS} " for transitions derivable with the SOS rules, and " \xrightarrow{SSOS} " for transitions derivable with the SSOS rules.

4 Characterization of the SSOS

Our SSOS semantics is essentially an operational semantics for symbolic execution of Stateflow programs. It opens up the opportunity for application of a broader spectrum of verification techniques, such as: testing (purely symbolic, or as a combination of symbolic and concrete (concolic) testing [23]) or bounded model checking [16]. To be able to reason symbolically over Stateflow programs, however, one must first provide a formal characterization of the relationship between its concrete and symbolic execution. In this section, we prove two results that characterize this relationship. In Theorem 1 we show that for each derivable SSOS transition there exists a corresponding derivable SOS transition. Conversely, in Theorem 2 we show that for each derivable SOS transition there exists a derivable SSOS transition. The connection is established in both cases by means of an interpretation of the symbolic values for which the Boolean expression added to the path condition holds.

First, we introduce some additional notation. Let $\beta: SEnv \times Env \to Env$ be a function that transforms a symbolic environment Δ into a concrete one $\beta(\Delta, D)$ with the help of an environment D that serves as an interpretation of the symbolic values; for any $v \in Var$, let $\beta(\Delta, D)(v)$ be defined as the value of the expression $\Delta(v)$ in the (renamed) environment $D \circ g^{-1}$. Similarly, let $\mathcal{B}: BExpr_{Sym} \to (Env \to Bool)$ be a function that evaluates path conditions in concrete environments, so that $\mathcal{B}[\![pc]\!](D)$ is the Boolean value of the path condition pc in $D \circ g^{-1}$. Finally, observing that the transitions derived by the SSOS rules only (potentially) add a conjunct to the current path condition pc_k to obtain a new path condition pc^{k+1} , let pc_k^{k+1} denote this added conjunct (or \top , if no conjunct is added).

Theorem 1. If $(P_1, \langle \Delta_1, pc_1 \rangle) \xrightarrow{SSOS} (P_2, \langle \Delta_2, pc_2 \rangle, tv)$, then for all $D_0 \in Env$ such that $\mathcal{B}[pc_1^2](\beta(\Delta_1, D_0)) = \top$, we have $(P_1, \beta(\Delta_1, D_0)) \xrightarrow{SOS} (P_2, \beta(\Delta_2, D_0))$.

Proof. For proving the result, we shall use the principle of *Rule Induction* (see, e.g., [24, p. 41]). The principle states that, in order to prove that a given predicate over judgements holds for all judgements derivable by a given set of rules, one has to show that every rule preserves the predicate. In the statement of the theorem, the predicate to be proved is the one defined by the then-clause.

The complete proof has thus to consider each of the 27 rules of the SSOS. We include here only a few selected cases of the proof to illustrate the proof technique. As it can be seen from the given cases, all of them follow the same pattern, so it should be clear to the reader how the complete proof unfolds.

Case 1.1. $[t-FIRE]_{SSOS}$

Since the two premises of the rule are not transitions over which the statement is proved, we can treat them as side conditions and assume them to be true (since otherwise one cannot apply the rule). Next, let $D_0 \in Env$ be such that:

$$\mathcal{B}[\![c]\!](\beta(\Delta_1, D_0)) = \top \tag{5}$$

and let $D_1, D_2 \in Env$. $D_1 = \beta(\Delta_1, D_0)$ and $D_2 = \beta(\Delta_2, D_0)$. Then, we have $e \vdash (c, D_1) \to D_1$ and $e \vdash (a, D_1) \to D_2$, and therefore $e \vdash (t, D_1) \to D_2$, Fire(d, ta), where $pc_2 = pc_1 \land c$ and $\Delta_2 = \mathcal{SA}[a](\Delta_1)$. The premises of the [t-Fire]_{SOS} rule are thus true, and we can apply the rule to obtain the following:

$$e \vdash (t, D_1) \rightarrow D_2, Fire(d, ta)$$

which is what needed to be demonstrated.

Case 1.2 $[T\text{-}FIRE]_{SSOS}$

Let $D_0 \in Env$, and t = t.T be such that:

$$\mathcal{B}[t.c](\beta(\Delta_1, D_0)) = \top \tag{6}$$

and let $D_1, D_2 \in Env$. $D_1 = \beta(\Delta_1, D_0), D_2 = \beta(\Delta_2, D_0)$. From (6), and the induction hypothesis it follows:

$$e, J \vdash (t, D_1) \to D_2, Fire(d, ta)$$
 (7)

Now that the premise for [T-FIRE]_{SOS} rule given in (7) is true, we can apply the rule to obtain the following:

$$e, J \vdash (t.T, D_1) \rightarrow D_2, Fire(d, ta)$$

which concludes the proof for the case.

Case 1.3 $[SD-FIRE]_{SSOS}$

Let $D_0 \in Env$ be such that:

$$\mathcal{B}[pc_1^2](\beta(\Delta_1, D_0)) = \top, \ \mathcal{B}[pc_2^3](\beta(\Delta_3, D_0)) = \top, \ \mathcal{B}[pc_3^4](\beta(\Delta_4, D_0)) = \top$$
(8)

where $pc_2 = pc_1 \wedge pc_1^2$, $pc_3 = pc_2 \wedge pc_2^3$, $pc_4 = pc_3 \wedge pc_3^4$. Based on (8), we know that

$$\exists D_1, \dots, D_5 \in Env : D_1 = \beta(\Delta_1, D_0), D_2 = \beta(\Delta_2, D_0), D_3 = \beta(\Delta_3, D_0),$$

 $D_4 = \beta(\Delta_4, D_0), D_5 = \beta(\Delta_5, D_0)$

From (8) and the induction hypothesis, it follows that:

$$e, J_0 \vdash (T_0, D_1) \to D_2, Fire(d, ta); \ e \vdash (a, D_2) \to D_3;$$

 $e, J \vdash (C, D_3) \to D_4; \ e, J \vdash (A.ex, D_4) \to D_5$ (9)

Since the premises for [SD-FIRE]_{SOS} rule given in (9) are true, we can apply the rule to obtain the following:

$$e, J_O \vdash ((A, C, T_i, T_o, J), D_1) \to ((A, C', T_i, T_o, J), D_5), Fire(d, \diamond)$$

which is what needed to be shown.

Case 1.4 [AND]_{SSOS} Let $D_0 \in Env$ be such that the following holds:

$$\forall i \in [1, \dots, n] \ . \ \mathcal{B}[pc_i^{i+1}](\beta(\Delta_i, D_0)) = \top; \tag{10}$$

From the initial assumption and (10), we know that:

$$\forall k \in [1, \ldots, n+1] : \exists D_k \in Env : D_k = \beta(\Delta_k, D_0)$$

From the induction hypothesis and (10), it follows that:

$$\forall i \in [1, \dots, n] \quad e, J \vdash (sd_i, D_i) \to sd'_i, D_{i+1}, No$$
(11)

Since the premise for the $[AND]_{SSOS}$ rule (11) is true, we can apply the rule to obtain the following:

$$e, J \vdash (e, J \vdash (And\{s_0 : sd_0 \cdots s_n : sd_n\}, D_1) \rightarrow (And\{s_0 : sd'_0 \cdots s_n : sd'_n\}, D_{n+1}, No)$$

which is what was needed to be shown.

Case $1.5 [OR-FIRE]_{SSOS}$

Let $D_0 \in Env$ be such that the following holds:

$$\mathcal{B}[pc_1^2](\beta(\Delta_1, D_0)) = \top; \tag{12}$$

Based on (12), the following holds:

$$\exists D_1, D_2 \in Env . D_1 = \beta(\Delta_1, D_0), D_2 = \beta(\Delta_2, D_0)$$

From the inductive hypothesis and (12), it follows that:

$$e, J \vdash (sd, D_1) \rightarrow (sd', D_2), Fire(p', a)$$
 (13)

Since the premise for the [OR-FIRE]_{SOS} rule (13) is true, we can apply the rule to obtain the following:

$$e, J \vdash (Or(s, p, T, SD[s:sd]), D_1, tv) \rightarrow (Or(\emptyset_s, p, T, SD[s:sd']), D_2, Fire(p', a))$$

which is what was needed to be shown.

Our next result establishes the reverse direction.

Theorem 2. If $(P_1, D_1) \xrightarrow{SOS} (P_2, D_2)$, then for all $pc_1 \in BExpr_{Sym}$, $\Delta_1 \in SEnv$ and $D_0 \in Env$ such that $\beta(\Delta_1, D_0) = D_1$, there exist pc_2 , $pc_1^2 \in BExpr_{Sym}$ and $\Delta_2 \in SEnv$ such that $pc_2 = pc_1 \land pc_1^2$, $\mathcal{B}[pc_1^2](\beta(\Delta_1, D_0)) = \top$, $\beta(\Delta_2, D_0) = D_2$ and $(P_1, \langle \Delta_1, pc_1 \rangle) \xrightarrow{SSOS} (P_2, \langle \Delta_2, pc_2 \rangle)$.

Proof. Again, the proof is by Rule Induction. We only show two cases here. The rest of the cases are proved by following the scheme of the presented ones.

Case 2.1. $[t-FIRE]_{SOS}$

Let us assume that a Stateflow program performs a concrete transition derivable using the [t-Fire]_{SOS} rule. Lets assume that for arbitrary $\langle \Delta_1, pc_1 \rangle \in SS$ the following holds:

$$\exists D_0 \in Env \, . \, \beta(\Delta_1, D_0) = D_1 \tag{14}$$

Also, we know that $pc_1^2 = \mathcal{B}[\![c]\!](f(\Delta_1, D_0)) = \top$, due to the inductive hypothesis. Consequently, the following also holds: $\mathcal{B}[\![pc_1 \wedge c]\!](f(\Delta_1, D_0)) = \top$. Based on (14) and the true valuation of $pc_1 \wedge c$, the following also holds:

$$e \vdash (t, \beta(\Delta_1, D_0)) \rightarrow \beta(\Delta_2, D_0), Fire(d, ta)$$

where $pc_2 = pc_1 \wedge c$ and $\Delta_2 = \mathcal{SA}[a](\Delta_1)$. Based on (14) and the induction hypothesis we derive the following: $e \vdash (c, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_1, pc_2 \rangle$ and $e \vdash (a, \langle \Delta_1, pc_2 \rangle) \rightarrow \langle \Delta_2, pc_2 \rangle$. If we now apply the [t-Fire]_{SSOS} rule over the last two premises, we derive the following transition:

$$e \vdash (t, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_2, pc_2 \rangle, Fire(d, ta))$$

which is what needed to be demonstrated.

Case $2.2 [T-FIRE]_{SOS}$

Let us assume that a Stateflow program performs a concrete transition derivable using the [T-Fire]_{SOS} rule. Let us assume that for an arbitrary $\langle \Delta_1, pc_1 \rangle \in SS$ the following holds:

$$\exists D_0 \in Env \, . \, \beta(\Delta_1, D_0) = D_1 \tag{15}$$

Also, we know that $pc_1^2 = \mathcal{B}[\![c]\!](\beta(\Delta_1, D_0)) = \top$, due to the inductive hypothesis. Consequently, the following also holds: $\mathcal{B}[\![pc_1 \land c]\!](\beta(\Delta_1, D_0)) = \top$. Based on (15) and the truth valuation of the $pc_1 \land c$, the following also holds:

$$e \vdash (t, \beta(\Delta_1, D_0)) \rightarrow \beta(\Delta_2, D_0), Fire(d, ta)$$

where $pc_2 = pc_1 \wedge c$ and $\Delta_2 = \mathcal{SA}[a](\Delta_1)$. Based on (15) and the induction hypothesis we derive the following: $e \vdash (c, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_1, pc_2 \rangle$ and $e \vdash (a, \langle \Delta_1, pc_2 \rangle) \rightarrow \langle \Delta_2, pc_2 \rangle$. If we now apply the [T-Fire]_{SSOS} rule over the last two premises, we derive the following transition:

$$e \vdash (t, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_2, pc_2 \rangle, Fire(d, ta)$$

which is what needed to be demonstrated.

There are two important corollaries of the above two results, which we will only state here informally. First, both results lift naturally to *executions*, i.e., to sequences of transitions. Note in particular how in Theorem 2 the "for all pc_1

... there exists pc_2 " part allows the sequential composition of transitions. Second, when starting from a true path condition, as one does in symbolic execution, the *satisfying assignments* for the path condition at the end of any symbolic path, viewed as interpreting environments, define precisely the concrete paths that follow the symbolic one.

Further, the executions in SOS and SSOS can be shown to *simulate* each other with respect to processing external events. It is well-known that invariant properties are preserved by simulation, and thus, can be checked by symbolically executing the given Stateflow program. Even if limited, this class of properties is important in industrial contexts, as our collaboration with Scania on formally verifying safety-critical embedded code generated from Simulink models has shown.

5 From Stateflow Programs to SMT Solving

In our work, we focus on using BMC for checking invariant properties over symbolic representation of Stateflow programs. In Section 3 we developed an SSOS for Stateflow, and exhibited in Section 4 a simulation relation between executions derived in SOS and SSOS, which is sufficient for the preservation of invariant properties. In the following, we show how we use the SSOS to relate Stateflow programs to STS over symbolic configurations. We define the k-bounded invariant checking problem for the latter representation (Section 5.1), and show how this problem can be encoded as an SMT problem (Section 5.2).

5.1 Bounded Invariant Checking for Stateflow Programs

In this section, we define a version of STS that encode the *symbolic* behaviors of Stateflow programs, and then adapt the BMC problem to such transition systems.

Definition 3 (STS over Symbolic Configurations). A symbolic transition system over the symbolic configurations of a given Stateflow program is an STS $\widehat{S} = (\widehat{I}, \widehat{R})$, in the sense of Definition 2, but over the symbolic configurations and transitions of the program as induced by the SSOS rules.

 $\widehat{I}(\cdot)$ and $\widehat{R}(\cdot,\cdot)$ are thus a unary "initialization" predicate and a binary "next-state" predicate over the symbolic configurations of the program, respectively, which are quantifier-free FOL formulas over the components of symbolic configurations.

The formal relationship between an STS over symbolic configurations \hat{S} and an ordinary STS S of a Stateflow program is given by the following result.

Proposition 1. Let SF be a Stateflow program, S=(I,R) be an STS over its concrete configurations as induced by the SOS rules, and $\widehat{S}=(\widehat{I},\widehat{R})$ be an STS over its symbolic configurations as induced by the SSOS rules. Then, the following equivalences hold:

$$(1) \ \widehat{I}(P,\langle pc,\Delta\rangle) \Leftrightarrow \exists D_0 \in Env. \ I(P,D_0) \land I(P,\beta(\Delta,D_0)) \land \mathcal{B}[\![pc]\!](\beta(\Delta,D_0))$$

(2)
$$\widehat{R}((P, \langle pc, \Delta \rangle), (P', \langle pc', \Delta' \rangle)) \Leftrightarrow \exists D_0 \in Env. \mathcal{B}[pc][(\beta(\Delta, D_0)) \land \mathcal{B}[pc']](\beta(\Delta', D_0)) \land R((P, \beta(\Delta, D_0)), (P', \beta(\Delta', D_0))))$$

Proof.

Direction (\Rightarrow) of (1).

Assume that $\widehat{I}(P, \langle \Delta, pc \rangle) = \top$. From the definition of initial symbolic configuration (Section 3), we know that that $pc = pc_0$ and $\Delta = \Delta_0$, and that $pc_0 = \top$ and $\Delta = g$. Since $pc = \top$, there must exist $D_0 \in Env$ such that $I(P, D_0)$, and $\beta(\Delta, D_0) = D$. Given that $\Delta = g$, it follows that $D = D_0$. Since $I(P, D_0) = \top$, then also $I(P, D) = \top$.

Direction (\Leftarrow) of (1).

Assume that there exists $D_0 \in Env$ such that $I(P, D_0)$, $\mathcal{B}[pc](\beta(\Delta, D_0)) = \top$, and $I(P, \beta(\Delta, D_0))$. If $I(P, \beta(\Delta, D_0)) = \top$, then $\beta(\Delta, D_0) = D$. $I(P, D_0) = \top$ and $I(P, D) = \top$ if and only if $D = D_0$. This is possible only if $\Delta = g$. Given the definition of initial symbolic configuration (Section 3), the assumption $\mathcal{B}[pc](\beta(\Delta, D_0)) = \top$, and $\Delta = g$, it follows that $\widehat{I}(P, \langle \Delta, pc \rangle) = \top$.

Direction (\Rightarrow) of (2).

Assume that $\widehat{R}((P,\langle \Delta_1,pc_1\rangle),(P',\langle \Delta_2,pc_2\rangle)) = \top$. According to Definition 3, if $\widehat{R}((P,\langle \Delta_1,pc_1\rangle),(P',\langle \Delta_2,pc_2\rangle)) = \top$, then there exists $(P,\langle \Delta_1,pc_1\rangle) \xrightarrow{SSOS} (P',\langle \Delta_2,pc_2\rangle)$. From Theorem 1, if $(P,\langle \Delta_1,pc_1\rangle) \xrightarrow{SSOS} (P',\langle \Delta_2,pc_2\rangle)$, then for all $D_0 \in Env$ such that $\mathcal{B}[pc_1](\beta(\Delta_1,D_0)) = \top$, and $\mathcal{B}[pc_1](\beta(\Delta_1,D_0)) = \top$, there is $(P,\beta(\Delta_1,D_0)) \xrightarrow{SOS} (P,\beta(\Delta_2,D_0))$. Using the result for the existence of an SOS transition, and Definition 2, we can conclude that:

$$R((P, \beta(\Delta_1, D_0)), (P, \beta(\Delta_2, D_0))) = \top$$

Direction (\Leftarrow) of (2).

Assume that there exists $D_0 \in Env$ such that $\mathcal{B}[\![pc_1]\!](\beta(\Delta_1, D_0)) = \top$, $\mathcal{B}[\![pc_2]\!](\beta(\Delta_2, D_0)) = \top$, and $R((P, \beta(\Delta_1, D_0)), (P', \beta(\Delta_2, D_0))) = \top$. From Definition 2, if $R((P, \beta(\Delta_1, D_0)), (P, \beta(\Delta_2, D_0))) = \top$, then $(P, \beta(\Delta_1, D_0)) \xrightarrow{SOS} (P, \beta(\Delta_2, D_0))$.

Based on the existence of $(P, \beta(\Delta_1, D_0)) \xrightarrow{SOS} (P, \beta(\Delta_2, D_0))$, and by following Theorem 2, we have that for all $pc_1 \in BExpr_{Sym}$, $\Delta_1 \in SEnv$, and $D_0 \in Env$ such that $\beta(\Delta_1, D_0) = D_1$, there exist $pc_1^2, pc_2 \in BExpr_{Sym}$, and $\Delta_2 \in SEnv$ such that $pc_2 = pc_1 \land pc_1^2$, $\mathcal{B}[pc_1^2](\beta(\Delta_1, D_0)) = \top$, $\beta(\Delta_2, D_0) = D_2$ and $(P, \langle \Delta_1, pc_1 \rangle) \xrightarrow{SSOS} (P', \langle \Delta_2, pc_2 \rangle)$. Using the result for the derived SSOS transition, and Definition 3, we can conclude that $\widehat{R}((P, \langle \Delta_1, pc_1 \rangle), (P', \langle \Delta_2, pc_2 \rangle)) = \top$.

Now, let φ be a predicate over the concrete configurations of a Stateflow program. Predicate φ induces a corresponding predicate $\widehat{\varphi}(sc) \triangleq \varphi(sc[g^{-1}])$ over the symbolic configurations $sc = (P, \langle pc, \Delta \rangle)$, where g is the bijection from Section 3. Assuming an interpretation for the path and k-bounded invariant property formulas for executions over symbolic configurations, the counter-example path formula (3) for symbolic executions can be rewritten as follows:

$$\exists sc_0, \dots, sc_k. \ (path(sc_0, \dots, sc_k) \land \bigvee_{i=0}^k \neg \widehat{\varphi}(sc_i))$$
 (16)

Based on formula (16), and along the lines of the definition of BMC for C programs given in [25], we derive the following.

Theorem 3. Let SF be a Stateflow program, $\widehat{S} = (\widehat{I}, \widehat{R})$ be an STS over its symbolic configurations, and φ^k be a k-bounded invariant property. Then, the following two statements are equivalent:

1. SF satisfies the k-bounded invariant property φ^k .

2. The formula
$$path(sc_0, \ldots sc_k) \wedge \bigvee_{i=0}^k \neg \widehat{\varphi}(sc_i)$$
 is UNSAT.

Proof. (By contradiction.) Assume that a given Stateflow program does not satisfy the k-bounded invariant property φ^k , and that statement (2) holds. From the definition of k-bounded invariant property, we know that such a property fails if there exists a path in which the last configuration violates φ . From Definition 16, we know that such a path exists if the formula given in (2) is satisfiable (SAT), which contradicts the initial assumption.

Assume that the formula in (2) is SAT, and that the statement (1) holds. The satisfiability of the formula says that there exists a reachable state sc_i . i < k, in which the negation of the property $\widehat{\varphi}$ holds, that is, $\widehat{\varphi}(sc_i) = \top$. In such case $\widehat{\varphi}^k$ is not an invariant, which contradicts our initial assumption.

Now that we have formally defined BMC invariant checking for STS over symbolic configurations, we show how to construct the predicates \hat{I} and \hat{R} for a given Stateflow program.

5.2 From Stateflow Programs to SMT Scripts

In this section, we describe a procedure for deriving an STS from a given Stateflow program using the set of SSOS rules, and the transformation of the STS predicates into quantifier-free FOL formulas that can be used for *k*-bounded invariant checking over symbolic configurations, as defined in Theorem 3.

Before deriving an STS $\widehat{S} = (\widehat{I}, \widehat{R})$ from a given Stateflow program, one must make a decision as to the *granularity* of the transitions in executions. At its highest syntactic level, any Stateflow program is an Or-composition or an And-composition. Since our running example is an Or-composition, and since

conceptually the procedure for And-compositions is virtually the same, in the following we focus on the Or-compositions only. We derive an STS in which the transitions between configurations correspond to transitions at the top Or-composition level. Due to the layered structure of the imperative language, each such transition consists of a series of transitions at the lower levels (see Table 1 from Section 2.2). Our approach to the derivation of the top-level transitions is to use our SSOS to perform symbolic execution between any possible pair of consecutive control points of the program, for arbitrary values of the data. One should note that in general case, the derivation of the STS is not strictly bound to the top-level component, as it can be done against any syntactic class of the Stateflow imperative language.

As a result of our adopted modeling principle, the configurations for the induced STS are of the following type: $(Or, \langle pc, \Delta \rangle)$. Even though the program component during execution remains the same (the top-level Or-component), it can be the case that its internal configuration changes. The internal configuration of an Or-component is characterized by the set of active substates. Consequently, the program control points correspond to the possible internal configurations at the top Or-composition level. For instance, our running State-flow example from Figure 1 has 5 program control points, namely: i) no active states, ii) Stop and Reset, iii) Stop and Lap_stop, iv) Run and Running, and v) Run and Lap. One way of modeling the program control points as defined above is to extend the set of variables Var with a set of auxiliary Boolean variables (Var_C) . For every control point Or, the set of variables Var_C can be partitioned into two subsets: the set $Var_{C^+} = \{v \mid v \in Var_C, v = \top\}$ corresponding to the active states of Or, and $Var_{C^-} = Var_C \setminus Var_{C^+}$. Thus, the control point Or is characterized by the formula:

$$\Phi_{Or} \triangleq \bigwedge_{v \in Var_{C^{+}}} v \wedge \bigwedge_{v \in Var_{C^{-}}} \neg v \tag{17}$$

The initial symbolic configuration for an STS of a Stateflow program given as an Or-composition is defined as $(Or_{\emptyset}, \langle pc_0, \Delta_0 \rangle)$, where: Or_{\emptyset} is an uninitialized Or-component that has no active substates, $pc_0 = \top$, and $\Delta = g$ (see Section 3). As explained, in our STS we only have transitions between symbolic configurations of the form $(Or, \langle pc, \Delta \rangle) \to (Or', \langle pc', \Delta' \rangle)$, where Or and Or' are consecutive program control points. To express the predicates \widehat{I} and \widehat{R} as quantifier-free FOL formulas, we need to construct the quantifier-free FOL formulas for the pc and Δ components of the symbolic configurations.

The path condition pc is a quantifier-free Boolean expression over symbols, and as such can be viewed as a quantifier-free FOL formula Φ_{pc} . The symbolic environment Δ , on the other hand, is a mapping between program variables and arithmetic expressions over symbols. From Δ , one can construct a quantifier-free FOL formula modulo theory of arithmetic for Φ_{Δ} , as follows:

$$\Phi_{\Delta} \triangleq \bigwedge_{v \in Var_D} v' = \Delta(v) \tag{18}$$

where $Var_D = Var \setminus Var_C$.

Now that we have defined the construction of quantifier-free FOL formulas for each of the components of the symbolic configurations of an STS, we can construct, for any transition T_i between symbolic configurations, a quantifier-free FOL formula (Φ_{T_i}) modulo theory of arithmetic, as follows:

$$\Phi_{T_i} \triangleq \Phi_{Or} \wedge \Phi_{pc_1^2} \Rightarrow \Phi_{Or'} \wedge \Phi_{\Delta'} \tag{19}$$

Finally, based on the formula (19) and Proposition 1, we encode the predicates \hat{I} and \hat{R} as the following quantifier-free FOL modulo theory of arithmetic formulas:

$$\widehat{I} \triangleq \Phi_{Or_{\emptyset}} \wedge \Phi_{\Delta_{0}}
\widehat{R} \triangleq \bigwedge_{T_{i} \in T} \Phi_{T_{i}}$$
(20)

where T is the set of all derivable SSOS transitions from the initial top-level composition. One way of computing T is to start from the initial program control point (Or_{\emptyset}) and derive all transitions between reachable program control points with a standard breadth-first or depth-first search algorithm.

The remaining step now is to encode the FOL formulas corresponding to the \widehat{I} and \widehat{R} predicates into the SMT-LIB script format, in order to be used as an input to an SMT-solver. We assume that the program data (Var_D) in the Stateflow program is composed of scalar variables, which are either numeric or Boolean. There are two ways of encoding the numeric variables for SMT: i) using the theory of bit-vectors [26], or ii) directly as variables of the sort that corresponds to their numeric domain. The bit-vector encoding provides an accurate way of capturing the binary representation of the numeric values; however, the resulting formula depends on the size of the vector. The numeric representation, on the other hand, provides an encoding independent of the binary representation, but does not guarantee completeness for non-linear arithmetic expressions. In our case, the latter suffices. We model the *events* using Boolean variables.

An earlier work shows how a sequence of C commands can be converted into an SMT script [25], using the concepts of conditional normal form, and single static assignment form (SSA) (see, e.g., [27]). A program is in conditional normal form if all the statements are of the form if (c) then r, where r is either an assertion or an assignment. The SSA encoding principle requires each variable to be assigned only once, which means that for each assignment a fresh variable is added to the set Var. For instance, v_1^1 is a fresh variable that represents the first intermediate assignment of the variable v during the second execution step. This resolves the SMT-encoding for the sequential execution of the accumulated actions from the derived transitions.

6 Practical Evaluation of SESf

In this section, we present the preliminary results from the initial practical evaluation of our approach (henceforth referred to as SESF). As benchmark, we use the Stopwatch running example from Section 2.1. The main purpose of this evaluation is to obtain preliminary data for the practical usefulness of our approach, in terms of its ability to detect design errors, and the time it requires to detect them. Even though the most natural way to assess the applicability and the practical usefulness of our approach is to benchmark it against the SLDV tool on a wider set of use cases, in the end it was not possible due to the licensing constraints described in Section 1.

To this date, we have automated the following aspects of our approach: the generation of an underlying STS $\widehat{S} = (\widehat{I}, \widehat{R})$, the incremental unrolling of the predicate \widehat{R} that models the program execution, and checking the satisfiability of the generated formula after each unrolling, using the Z3 solver. The only part that is not yet automated is the SMT-LIB encoding of the underlying STS.

Before we delve into the analysis, we explain the environmental setup for the evaluation. Our SESF tool requires that the Stateflow model is encoded into the imperative language, which is then provided as an input to the SESF tool. As the transformation of the graphical model into an imperative program is beyond the scope of this paper, we assume that there exists a tool that converts an original Stateflow model into a program written in the imperative language. The complete evaluation is performed on a standard MacBook Pro workstation with a 2,4 GHz Quad-Core Intel Core i5 processor and 8GB of RAM memory. We use Matlab version R2020b.

The set T of symbolic transitions at the program level is generated by a symbolic execution engine which is implemented in Python. For each of the transitions, the symbolic execution engine generates a certificate for the correctness of the transition in the form of an SSOS derivation tree [28]. The tool generates the set T for the Stopwatch program, consisting of 22 transitions, within one second. Next, we manually encode the set T into SMT-LIB assertions, by following the procedure described in Section 5.2.

Next, we proceed to the main part of the evaluation, where we measure the time required by the tool to find a violation for a given invariant property, i.e., a counter-example, which is completely automated. In order to test the time for finding a counter-example, we analyze the Stopwatch model against the following parametric invariant property: The value of cent is always between 0 and X, for which we know that there exists a counter-example trace of a specific length since the cent variables will increase up to 100. We then run the analysis for the following set of values for X: $\{5, 10, 20, 40, 80, 99\}$. The SESF was able to provide a counter-example for each instance of the property. The results, in terms of time required to find the counter-example for each of the values of X, are given in Figure 4.

Following the data plot, we can see that the SESF tool was able to find violation for all the parameter values for the parametric safety property in a time frame ranging from several seconds to less than two minutes. Even though the

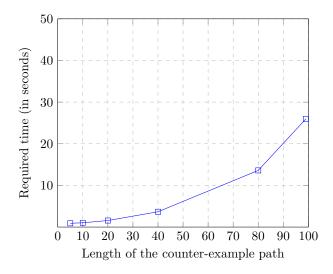


Figure 4: Evaluation results

obtained data can be best judged in a head-to-head comparison with the state-of-the-practice tool for formal verification of Stateflow models, we are unable to do so due to the aforementioned licensing issues. In spite of the limited validation presented in this section, we deem our approach and the prototype implementation to have potential for practical application as it was able to find the injected design errors within a reasonable time frame.

7 Related Work

To the best of our knowledge, the present work is the first to define a symbolic operational semantics for any kind of Stateflow language, and a corresponding notion of STS over symbolic configurations. The main goal of this paper has been to establish the foundations for provably correct verification of Stateflow models using symbolic methods, while the development of a practical tool and its extensive evaluation has mostly been left as future work. Still, in this section we will outline some of the state-of-the-art approaches and tools for the formal analysis and verification of Stateflow models, and will draw some high-level parallels to our work.

The most common way of formally verifying Stateflow models is to propose and apply transformation rules for the basic Stateflow modeling constructs into some existing formal framework. Yang et al. [12, 13] propose a technique for transforming Stateflow models into UPPAAL timed automata. The main idea of the technique is that by translating a Stateflow model into a timed automaton one creates a timed model, which can then be subjected to verification with respect to timing and liveness properties, based on an informal semantics of the Stateflow language. A similar endeavour is by Chen [29], where an approach

for formal analysis of Stateflow diagrams based on the PAL model checker is presented. Although claimed by the author that all of the Stateflow modeling principles are covered, the correctness of the formal model cannot be formally demonstrated. Meenakshi et al. [30] propose a tool for the transformation of Simulink models into the input language of a model checker. Similarly to the previous work, the main limitation of this work is proving the correctness of the transformation. Miyazawa et al. [31] provide a formalization of Stateflow in a refinement language called Circus. The main difference with our work lies in the fact that the they define formal semantics specific for the Circus language, whereas in our case the semantics are defined in generalized SOS-style. The consequence of this is the usability of the semantics, which in the case of Miyazawa et al. [31] are tool-specific, whereas in our case the proposed SSOS are not particularly bounded to any verification method or tool.

There exists a set of approaches that treat Stateflow models as hybrid system models, and apply corresponding modelling and analysis approaches. In this category, we can mention the approach by Alur et al. [32], which uses a combination of numerical simulation and symbolic analysis for Simulink/Stateflow models for improving the simulation coverage of the models. At the core of their technique lies a transformation of Simulink/Stateflow models into linear hybrid systems, which are then used for the analysis of the models using backward image computation, in order to identify classes of inputs, which can then be analyzed using minimal numerical simulations. Zuliani et al. [33] employ statistical model checking (SMC) based on Bayesian statistics to reason about the correctness of Stateflow models. By resorting to SMC they alleviate the statespace explosion problem of complex Stateflow models; however, the obtained results are in a form of probabilistic bounds, which cannot be directly applied for refining the Stateflow models. Duggirala et al. [34] propose the C2E2 tool, which can be used for verification of a broad class of hybrid and dynamic system models based on validated simulations. In contrast, the aim of our work is to establish the foundations for a symbolic analysis of completely discrete-time Stateflow models.

There is a body of work that is focused on defining a denotational semantics for Stateflow models. Hamon [10] proposed a denotational semantics for the Stateflow imperative language, as a complement to the already existing SOS semantics, in order to formalize the compilation of Stateflow programs. The denotational semantics is particularly useful for code generation from Stateflow models. In later work, Bourbouh et al. [11,35] extended and upgraded the denotational semantics of Hamon, and employed it for the compilation of Stateflow models into hierarchical state machines, and later also into Lustre models, for SMT-based formal analysis and verification, as well as for code generation. As this approach is very similar to ours, we aim to perform in the future a more detailed comparison, especially in terms of the generated analysis model as it is generated based on a different type of semantics. Auto-Gene [36] is an opensource toolset intended for code generation from high level modelling languages, including Simulink and Stateflow. The aim of the toolset is to use among others Simulink and Stateflow as input languages, from which it will automatically

generate C code. It uses only a safe subset of the Simulink/Stateflow language in order to be able to provide support for the verification of properties such as termination, context independence, etc.

8 Conclusion

We presented a technique for symbolic analysis of Stateflow programs with respect to invariant properties using bounded model checking (BMC). To this end, we developed a symbolic structural operational semantics (SSOS) for the Stateflow language. Our semantics is built on top of the work by Hamon and Rushby [8, 9], by converting each of their rules, in a uniform manner, into a symbolic form. We characterized the relationship between the two semantics by exhibiting a simulation relation between them. Next, we defined the bounded invariant checking problem for STS over symbolic configurations, as induced for a given Stateflow program by the set of the SSOS operational rules, and presented informally a procedure for deriving the initial and next state predicates of the STS. Finally, we showed how to generate, from the STS, a set of quantifier-free FOL assertions in SMT-LIB format suitable for analysis using state-of-the-art SMT solvers. The main benefit of our work is that it lays down the foundations for the development of tools for the scalable verification of complex industrial Stateflow models by means of existing symbolic techniques, which we demonstrated with bounded invariant checking on the running Stateflow program example for different values of the depth bound k. We showed that even though in its current state the tool cannot exhaustively verify Stateflow models, it still represents a viable choice for fast detection of design errors within a traces of finite length. Even though we initially planned to compare our approach against the state-of-the-practice SLDV tool, we had to withdraw from our idea once we discovered the license constraints imposed by Mathworks. Unfortunately, this happened in the final stage of this work, when it was already too late to steer the validation into a different direction.

Currently, we are working on automating the remaining parts of our technique, and packaging it into a user-friendly and easy to use tool-set. Our goal is to fully automate the transformation of the Stateflow programs into a symbolic representation, to be used as input to (bounded) invariant checkers, either applied to Stateflow programs in isolation, or as part of already existing frameworks for symbolic analysis of Simulink models (see, e.g., [37]). Once we have fully developed the tool-set, we will perform a more extensive evaluation of the approach, and compare its performance with that of state-of-the-art tools implementing alternative verification techniques. Further, we plan to extend our formal characterization of the SSOS in terms of a stronger equivalence between the concrete and symbolic representations of Stateflow programs, to formally underpin the symbolic verification of a wider class of properties than invariant properties, such as LTL properties. Finally, we will explore the possibility of extending our BMC approach from refutation-based to a verification one, by adding induction [21]. Along this line of research, we plan to include the option

of converting the generated STS into an input format for tools that implement more sophisticated model checking algorithms, such as Lustre [38] models for the Kind2 model checker [39].

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$$\frac{(e_{0} = e) \vee (e = \emptyset)}{e \vdash (c, \langle \Delta_{1}, pc_{1} \rangle) \rightarrow \langle \Delta_{1}, pc_{2} \rangle} \quad e \vdash (ca, \langle \Delta_{1}, pc_{2} \rangle) \hookrightarrow \langle \Delta_{2}, pc_{2} \rangle}{e \vdash ((e_{0}, c, ca, ta, d), \langle \Delta_{1}, pc_{1} \rangle) \rightarrow \langle \Delta_{2}, pc_{2} \rangle, Fire(d, ta)}$$
(a) [t-FIRE]_{SSOS} rule
$$(e_{0} \neq e) \wedge (e \neq \emptyset) \quad e \vdash ((e_{0}, c, ca, ta, d), \langle \Delta_{1}, pc_{1} \rangle) \rightarrow \langle \Delta_{1}, pc_{1} \rangle, No$$
(b) [t-NOT-ENABLED]_{SSOS} rule
$$\frac{(e_{0} = e) \vee (e = \emptyset) \quad e \vdash (\neg c, \langle \Delta_{1}, pc_{1} \rangle) \rightarrow \langle \Delta_{1}, pc_{2} \rangle}{e \vdash ((e_{0}, c, ca, ta, d), \langle \Delta_{1}, pc_{1} \rangle) \rightarrow \langle \Delta_{1}, pc_{2} \rangle, No}$$
(c) [t-NO-FIRE]_{SSOS} rule

Figure 5: SSOS rules for transitions.

A Complete Set of SSOS Rules

In this appendix, we present the complete set of SSOS rules. The rules are divided in 5 categories, as follows: rules for transitions (Figure 5), transition lists (Figure 6), state definitions (Figure 7), And-compositions (Figure 8), and Or-compositions (Figure 9, and Figure 10).

The set of SSOS rules for transitions is composed of 3 rules and is given in Figure 5. Intuitively, the rules describe the following. The [t-FIRE] rule describes how a Stateflow transition t fires by appending the symbolic evaluation of the condition t.c to the current path condition and by symbolically executing the condition action t.ca over the current symbolic environment Δ . When a transition fires, a transition event Fire(t.d, t.ta) is generated. The [t-NOT-ENABLED] rule is applicable when the event of the currently processed transition does not match the currently active event. Finally, the last rule [t-NO-FIRE] describes how a Stateflow transition does not fire because its guard does not evaluate to true. The processing of such transitions results in a new path condition, which is generated by appending the negation of the transition condition to the current path condition, while the symbolic environment remains the same.

Next comes the set of SSOS rules for transitions lists T, which is given in Figure 6. The list is composed of 7 rules, as follows. [T- \emptyset] describes how an empty transition list is processed, rules [T-FIRE], [T-NO-LAST-1], [T-NO-LAST-2], and [T-NO], describe the sequential execution of the constituent transitions of the transition list. If a transition from a transition list fires to a junction (j), the list of transitions of that junction is processed in the following manner: the instantaneous processing of transitions until a transition is completed ([T-FIRE-J-F]), a final junction is reached ([T-END]) or the evaluation fails in which case the execution backtracks to resume using the [T-FIRE-J-N] rule.

The SSOS rules for state definitions (5 in total) are given in Figure 7. The

$$\frac{T = \emptyset_T}{e, J \vdash (T, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_1, pc_1 \rangle, End}}{(a) \ T \cdot \emptyset_T \ \text{ssos} \ \text{rule}}$$

$$\frac{e \vdash (t, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_2, pc_2 \rangle, Fire(d, ta)}{e, J \vdash (t.T, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_2, pc_2 \rangle, Fire(d, ta)}}{(b) \ T \cdot FIRE \ \text{ssos} \ \text{rule}}$$

$$\frac{(e = t.e_0) \lor (t.e_0 = \emptyset) \quad e \vdash (t, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_1, pc_2 \rangle, No}{e, J \vdash (t.\emptyset, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_1, pc_2 \rangle, No}}$$

$$(c) \ T \cdot \text{NO} \cdot \text{LAST} \ \text{ssos} \ \text{rule}}$$

$$\frac{T \neq \emptyset_T \land ((e = t.e_0) \lor (t.e_0 = \emptyset))}{e \vdash (t, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_1, pc_2 \rangle, No}}$$

$$(c) \ T \cdot \text{NO} \cdot \text{LAST} \ \text{ssos} \ \text{rule}}$$

$$\frac{T \neq \emptyset_T \land ((e = t.e_0) \lor (t.e_0 = \emptyset))}{e \vdash (t, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_2, pc_3 \rangle, tv}}$$

$$(d) \ T \cdot \text{NO} \ \text{ssos} \ \text{rule}}$$

$$\frac{T \neq \emptyset_T \quad e \vdash (t, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_2, pc_2 \rangle, Fire(j, ta_1)}{e, J[j : T_j] \vdash (T_j, \langle \Delta_2, pc_2 \rangle) \rightarrow \langle \Delta_3, pc_3 \rangle, Fire(d, ta_2)}}{e, J[j : T_j] \vdash (T_j, \langle \Delta_2, pc_2 \rangle) \rightarrow \langle \Delta_3, pc_3 \rangle, Fire(d, ta_1; ta_2)}}$$

$$\frac{e \vdash (t, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_2, pc_2 \rangle, Fire(j, ta)}{e, J[j : T_j] \vdash (t.T, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_3, pc_3 \rangle, End}}{e, J[j : T_j] \vdash (t.T, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_3, pc_3 \rangle, Fire(j, ta)}}$$

$$\frac{e \vdash (t, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_2, pc_2 \rangle, Fire(j, ta)}{e, J[j : T_j] \vdash (T_j, \langle \Delta_2, pc_2 \rangle) \rightarrow \langle \Delta_3, pc_3 \rangle, End}}{e, J[j : T_j] \vdash (T_j, \langle \Delta_2, pc_2 \rangle) \rightarrow \langle \Delta_3, pc_3 \rangle, No}}$$

$$\frac{e \vdash (t, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_2, pc_2 \rangle, Fire(j, ta)}{e, J[j : T_j] \vdash (T_j, \langle \Delta_2, pc_2 \rangle) \rightarrow \langle \Delta_3, pc_3 \rangle, No}}{e, J[j : T_j] \vdash (T_j, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_4, pc_4 \rangle, tv}}$$

$$\frac{e \vdash (t, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_2, pc_2 \rangle, Fire(j, ta)}{e, J[j : T_j] \vdash (T_j, \langle \Delta_3, pc_3 \rangle) \rightarrow \langle \Delta_4, pc_4 \rangle, tv}}{e, J[j : T_j] \vdash (T_j, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_4, pc_4 \rangle, tv}}$$

Figure 6: SSOS rules for transition list (part 1).

first rule, named [SD-NO], is applied when there is no enabled transition for the state definition, be it internal or output. The rule [SD-INT-FIRE] applies when some internal transition within a state definition fires, whereas [SD-FIRE] captures the behavior of the state definition when one of its outgoing transitions fires. [SD-INIT] represents the initialization behavior of the state definitions,

$$e, J_o \vdash (T_o, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_2, pc_2 \rangle, tv \quad (tv = No) \lor (tv = End)$$

$$e \vdash (A.d, \langle \Delta_2, pc_2 \rangle) \hookrightarrow \langle \Delta_3, pc_2 \rangle \quad e, J \vdash (T_i, \langle \Delta_3, pc_2 \rangle) \rightarrow \langle \Delta_4, pc_3 \rangle, tv$$

$$e, J \vdash (C, \langle \Delta_4, pc_3 \rangle) \rightarrow \langle \Delta_5, pc_4 \rangle, No$$

$$e, J_O \vdash ((A, C, T_i, T_o, J), \langle \Delta_1, pc_1 \rangle) \rightarrow ((A, C', T_i, T_o, J), \langle \Delta_5, pc_4 \rangle), No$$

$$(a) \text{ SD-NO }_{\text{SSOS}} \text{ rule}$$

$$e, J_o \vdash (T_o, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_2, pc_2 \rangle, tv \quad (tv = No) \lor (tv = End)$$

$$e \vdash (A.d, \langle \Delta_2, pc_2 \rangle) \rightarrow \langle \Delta_3, pc_2 \rangle \quad e, J \vdash (T_i, \langle \Delta_3, pc_2 \rangle) \rightarrow \langle \Delta_4, pc_3 \rangle, tv$$

$$e, J \vdash (C, \langle \Delta_4, pc_3 \rangle) \rightarrow \langle \Delta_5, pc_4 \rangle, Fire(d, a)$$

$$e \vdash (a, \langle \Delta_5, pc_4 \rangle) \rightarrow \langle \Delta_6, pc_4 \rangle \quad e \vdash (A.ex, \langle \Delta_6, pc_4 \rangle) \rightarrow \langle \Delta_7, pc_4 \rangle$$

$$e, J_O \vdash ((A, C, T_i, T_o, J), \langle \Delta_1, pc \rangle) \rightarrow ((A, C', T_i, T_o, J), \langle \Delta_7, pc_4 \rangle), Fire(d, \diamond)$$

$$(b) \text{ SD-INT-FIRE }_{\text{SSOS}} \text{ rule}$$

$$e, J_O \vdash (T_o, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_2, pc_2 \rangle, Fire(d, ta) \quad e \vdash (ta, \langle \Delta_2, pc_2 \rangle) \rightarrow \langle \Delta_3, pc_2 \rangle$$

$$e \vdash (C, \langle \Delta_3, pc_2 \rangle) \Downarrow (C', \langle \Delta_4, pc_3 \rangle) \quad e \vdash (A.x, \langle \Delta_4, pc_3 \rangle) \rightarrow \langle \Delta_5, pc_3 \rangle$$

$$e, J_O \vdash ((A, C, T_i, T_o, J), \langle \Delta_1, pc_1 \rangle) \rightarrow ((A, C', T_i, T_o, J), \langle \Delta_5, pc_3 \rangle), Fire(d, \diamond)$$

$$(c) \text{ SD-FIRE }_{\text{SSOS}} \text{ rule}$$

$$e \vdash (A.en, \langle \Delta_1, pc_1 \rangle) \rightarrow (\langle \Delta_2, pc_1 \rangle) \quad e, p \vdash (C, \langle \Delta_2, pc_1 \rangle) \uparrow (C', \langle \Delta_3, pc_2 \rangle)$$

$$e, p \vdash ((A, C, T_i, T_o, J), \langle \Delta_1, pc_1 \rangle) \uparrow (A, C', T_i, T_o, J), \langle \Delta_3, pc_2 \rangle$$

$$(d) \text{ SD-INIT }_{\text{SSOS}} \text{ rule}$$

$$e, p \vdash ((C, \langle \Delta_1, pc_1 \rangle) \Downarrow (C', \langle \Delta_2, pc_2 \rangle) \quad e \vdash (A.ex, \langle \Delta_2, pc_2 \rangle) \rightarrow \langle \Delta_3, pc_2 \rangle$$

$$e, p \vdash ((A, C, T_i, T_o, J), \langle \Delta_1, pc_1 \rangle) \downarrow (A, C', T_i, T_o, J), \langle \Delta_3, pc_2 \rangle$$

$$e, p \vdash ((A, C, T_i, T_o, J), \langle \Delta_1, pc_1 \rangle) \downarrow (A, C', T_i, T_o, J), \langle \Delta_3, pc_2 \rangle$$

$$e, p \vdash ((A, C, T_i, T_o, J), \langle \Delta_1, pc_1 \rangle) \downarrow (A, C', T_i, T_o, J), \langle \Delta_3, pc_2 \rangle$$

$$e, p \vdash ((A, C, T_i, T_o, J), \langle \Delta_1, pc_1 \rangle) \downarrow (A, C', T_i, T_o, J), \langle \Delta_3, pc_2 \rangle$$

$$e, p \vdash ((A, C, T_i, T_o, J), \langle \Delta_1, pc_1 \rangle) \downarrow (A, C', T_i, T_o, J), \langle \Delta_3, pc_2 \rangle$$

$$e, p \vdash ((A, C, T_i, T_o, J), \langle \Delta_1, pc_1 \rangle) \downarrow (A, C', T_i, T_o, J), \langle \Delta_3, pc_2 \rangle$$

$$e, p \vdash ((A, C, T_i, T_o, J), \langle \Delta_1, pc_1 \rangle) \downarrow (A, C', T_i, T_o, J), \langle \Delta_3, pc_2 \rangle$$

$$e, p \vdash ((A, C,$$

Figure 7: SSOS rules for state definition.

and finally [SD-EXIT] describes the sequence of actions when a state definition is exited, when the state definition is placed inside another state.

The set of SSOS rules for And-compositions is given in Figure 8, and contains the rules for updating ([AND]), initialization ([AND-INIT]) and exiting ([AND-EXIT]), respectively.

Finally, the set of SSOS rules for Or-compositions is given in Figure 9, and Figure 10, respectively. Rules [OR-EXT-FIRE], and [OR-EXT-FIRE] describe the situations where a transition within an Or-composition fires and the destination is within and outside the composition, respectively. The [OR-NO] rule describes a situation when no transition of an Or-composition fires, whereas the [OR-INT-FIRE] and [OR-FIRE] rules are applicable when an internal or external transition fires, respectively. The [OR-INT-NO-STATE] rule describes how an Or-composition without any internal components fires. The initialization of an Or-composition in situations when the composition enters the initial state,

$$(tv = No) \lor (tv = End)$$

$$\forall i \in [1, \dots n] \quad e, J \vdash (sd_i, \langle \Delta_i, pc_i \rangle) \rightarrow (sd'_i, \langle \Delta_{i+1}, pc_{i+1} \rangle, No)$$

$$e, J, tv \vdash (And\{s_0 : sd_0 \cdots s_n : sd_n\}, \langle \Delta_1, pc_1 \rangle)$$

$$\rightarrow (And\{s_0 : sd'_0 \cdots s_n : sd'_n\}, \langle \Delta_{n+1}, pc_{n+1} \rangle), No$$

$$(a) \text{ AND }_{SSOS} \text{ rule}$$

$$p_j = p \quad \forall i \neq j, p_i = \emptyset_p$$

$$\forall i \in [1, \dots n] \quad e, p_i \vdash (sd_i, \langle \Delta_i, pc_i \rangle) \uparrow (sd_{i+1}, \langle \Delta_{i+1}, pc_{i+1} \rangle)$$

$$e, s_j, p \vdash (And(s_0 : sd_0 \cdots s_n : sd_n), \langle \Delta_1, pc_1 \rangle)$$

$$\uparrow (And\{s_0 : sd'_0 \cdots s_n : sd'_n\}, \langle \Delta_{n+1}, pc_{n+1} \rangle)$$

$$(b) \text{ AND-INIT }_{SSOS} \text{ rule}$$

$$\forall i \in [1, \dots n] \quad e \vdash (sd_{n-1}, \langle \Delta_i, pc_i \rangle, tv) \Downarrow (sd'_{n-1}, \langle \Delta_{i+1}, pc_{i+1} \rangle)$$

$$e \vdash (And\{s_0 : sd'_0 \cdots s_n : sd_n\}, \langle \Delta_1, pc_1 \rangle)$$

$$\Downarrow (And\{s_0 : sd'_0 \cdots s_n : sd'_n\}, \langle \Delta_{n+1}, pc_{n+1} \rangle)$$

$$(c) \text{ AND-EXIT }_{SSOS} \text{ rule}$$

Figure 8: SSOS rules for And composition.

or an incoming transition comes into non-initial internal state, is handled by the [OR-INIT- \emptyset_p] and [OR-INIT] rules. Finally, the [OR-EXIT] rule describes the steps involved in exiting from an Or-composition.

$$e \vdash (a, \langle \Delta_1, pc_1 \rangle) \hookrightarrow (\langle \Delta_2, pc_1 \rangle) \quad e \vdash (sd_0, \langle \Delta_2, pc_1 \rangle) \Downarrow (sd'_0, \langle \Delta_3, pc_2 \rangle) \\ e, p' \vdash (sd_1, \langle \Delta_3, pc_2 \rangle) \Uparrow (sd'_1, \langle \Delta_4, pc_3 \rangle) \\ \hline e, J, Fire(p.s., p', a)) \vdash (Or(s_0, p, T, SD[s_0: sd_0; s_1: sd_1]), \langle \Delta_1, pc_1 \rangle \\ \rightarrow (Or(s_1, p, T, SD[s_0: sd'_0; s_1: sd'_1]), \langle \Delta_4, pc_3 \rangle), No \\ (a) [OR-EXT-FIRE]_{SSOS} \text{ rule} \\ \hline \\ \frac{\neg prefix(p, p')}{e, p' \vdash (sd_0, \langle \Delta_2, pc_1 \rangle) \Downarrow (sd'_0, \langle \Delta_3, pc_2 \rangle)} \\ e, J \vdash (Or(s_0, p, T, SD[s: sd]), \langle \Delta_1, pc_1 \rangle) \\ \rightarrow (Or(s, p, T, SD[s: sd']), \langle \Delta_3, pc_2 \rangle), Fire(p', \diamond) \\ (b) [OR-EXT-FIRE-OUT]_{SSOS} \text{ rule} \\ \hline \\ \frac{(tv = No) \lor (tv = End)}{e, J \vdash (Sd_0, \langle \Delta_1, pc_1 \rangle)} \rightarrow sd'_0, \langle \Delta_2, pc_2 \rangle, No \\ e, J, tv \vdash (Or(s_0, p, T, SD[s: sd']), \langle \Delta_1, pc_1 \rangle) \\ \rightarrow (Or(s_0, p, T, SD[s: sd']), \langle \Delta_2, pc_2 \rangle), No \\ (c) [OR-NO]_{SSOS} \text{ rule} \\ \hline \\ (tv = No) \lor (tv = End) \quad e, J \vdash (sd_0, \langle \Delta_1, pc_1 \rangle, tv) \rightarrow (sd'_0, \langle \Delta_2, pc_2 \rangle, Fire(p', a)) \\ p' = p.s_1.p'' \quad e, p'' \vdash (sd_1\langle \Delta_2, pc_3 \rangle) \Uparrow (sd_1\langle \Delta_3, pc_3 \rangle) \\ \hline e, J, tv \vdash (Or(s_0, p, T, SD[s_0: sd_0; s_1: sd_1]), \langle \Delta_1, pc_1 \rangle) \\ \rightarrow (Or(s_1, p, s_1, T, SD[s_0: sd_0; s_1: sd_1]), \langle \Delta_2, pc_3 \rangle), No \\ (d) [OR-INT-FIRE]_{SSOS} \text{ rule} \\ \hline \\ (tv = No) \lor (tv = End) \quad e, J \vdash (sd, \langle \Delta_1, pc_1 \rangle, tv) \rightarrow (sd', \langle \Delta_2, pc_2 \rangle, Fire(p', a)) \\ \neg prefix(p', p) \\ \hline \\ e, J \vdash (Or(s, p, T, SD[s: sd]), \langle \Delta_1, pc_1 \rangle, tv) \\ \rightarrow (Or(\emptyset_s, p, T, SD[s: sd']), \langle \Delta_2, pc_2 \rangle), Fire(p', a) \\ (e) [OR-FIRE]_{SSOS} \text{ rule} \\ \hline \\ e, \emptyset_p \vdash (Or(\emptyset_s, p, \emptyset_T, []), \langle \Delta_1, pc_1 \rangle), tv \Uparrow (Or(\emptyset_s, p, \emptyset_T, []), \langle \Delta_1, pc_1 \rangle), tv \\ (f) [OR-INIT-NO-STATE]_{SSOS} \text{ rule}$$

Figure 9: SSOS rules for Or composition (part 1).

$$\begin{array}{c} e, J \vdash (T, \langle \Delta_1, pc_1 \rangle) \rightarrow \langle \Delta_2, pc_2 \rangle, Fire(s.p, a) \\ e, p \vdash (sd, \langle \Delta_2, pc_2 \rangle) \uparrow (sd, \langle \Delta_3, pc_3 \rangle) & e \vdash (a, \langle \Delta_3, pc_3 \rangle) \hookrightarrow \langle \Delta_4, pc_3 \rangle \\ \hline e, \emptyset_p \vdash (Or(\emptyset_s, p_0, \emptyset_T, [s:sd]), \langle \Delta_1, pc_1 \rangle) \uparrow Or(s, p_0, \emptyset_T, [s:sd']), \langle \Delta_4, pc_3 \rangle \\ & (g) \ [\text{OR-INIT-}\emptyset_p]_{\text{SSOS}} \ \text{rule} \\ \hline \\ e, p \vdash (sd, \langle \Delta, pc \rangle) \uparrow (sd', \langle \Delta_1, pc_1 \rangle) \\ \hline e, \emptyset_p \vdash Or(\emptyset_s, p_0, \emptyset_T, [s:sd]), \langle \Delta, pc \rangle \uparrow Or(s, p_0, \emptyset_T, [s:sd']), \langle \Delta_1, pc_1 \rangle \\ & (h) \ [\text{OR-INIT]}_{\text{SSOS}} \ \text{rule} \\ \hline \\ e, p \vdash (sd, \langle \Delta, pc \rangle) \downarrow (sd', \langle \Delta_1, pc_1 \rangle) \\ \hline e, \emptyset_p \vdash Or(s, p, \emptyset_T, [s:sd]), \langle \Delta, pc \rangle \downarrow Or(\emptyset_s, p, \emptyset_T, [s:sd']), \langle \Delta_1, pc_1 \rangle \\ & (i) \ [\text{OR-EXIT]}_{\text{SSOS}} \ \text{rule} \end{array}$$

Figure 10: SSOS rules for Or composition (part 2).