

## Publications

- [1] M. Badawi, L. Zhonghai, and A. Hemani, “Elastic management and qos provisioning scheme for adaptable multi-core protocol processing architecture,” in *2016 The 19th Euromicro Conference on Digital Systems Design. Proceedings*, 8 2016.
- [2] —, “Service-guaranteed multi-port packet memory for parallel protocol processing architecture,” in *2016 24th Euromicro International Conference on Parallel, Distributed, and Network-Based Processing. Proceedings*, 2 2016.
- [3] M. Badawi, A. Hemani, and L. Zhonghai, “Customizable coarse-grained energy-efficient reconfigurable packet processing architecture,” in *25th IEEE International Conference on Application-specific Systems, Architectures and Processors. Proceedings*, 6 2014.
- [4] M. Badawi, L. Huisheng, Z. Zhonghai, and A. Hemani, “Programmable protocol processing engine in heterogeneous mp-soc: A case study,” in *5th International Workshop on Highly Efficient Accelerators and Reconfigurable Technologies. Proceedings*, 6 2014.
- [5] M. Badawi and A. Hemani, “A coarse-grained reconfigurable protocol processor,” in *International Symposium on System-on-Chip, 2011. Proceedings*, 10-11 2011.
- [6] S. Penolazzi, M. Badawi, and A. Hemani, “A step beyond tlm : Inferring architectural transactions at functional untimed level,” in *IFIP/IEEE VLSI-SoC 2008 International Conference : 16th International Conference on Very Large Scale Integration*, 2008, pp. 505–509.
- [7] S. Penolazzi, A. Hemani, and M. Badawi, “Modelling embedded systems at functional untimed application level,” in *IP Conference (IP07)*, 2007, pp. 107–112.